



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫

NAAC - 'A' Grade accredited Institute (CGPA : 3.21)

MHRD NIRF-2019 Rank - 180

website: www.kitsw.ac.in

e-mail: principal.kitswgl@gmail.com

☎ : +91 870 2564888

Cell : +91 73825 20585

REPORT on Online National FDP

1- WEEK ONLINE FACULTY DEVELOPMENT PROGRAMME

Micro Grid, Electric Vehicles and Allied Areas – 2020
(MGEVAA-2020)

1 – 5 June, 2020

Organized by

DEPARTMENT OF
ELECTRICAL & ELECTRONICS ENGINEERING

IN ASSOCIATION WITH
ISTE - KITSW

Sponsored by:



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FDP BROCHURE

ISTE Sponsored One Week Faculty Development Program (Online) on Micro Grid, Electric Vehicles and Allied Areas

1st- 5th June 2020

Organized by
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

ABOUT THE INSTITUTE:

Kakatiya Institute of Technology & Science (KITS), Warangal was established in 1980; affiliated to Kakatiya University, Warangal and it became Autonomous Institution under Kakatiya University w.e.f year 2014. It is one of the premier institutions in the state of Telangana. It has attracted academicians of proven competence onto its faculty, placed its products in reputed organizations all over the World and gained recognition amongst academic circles. The Institute aims to prepare the students for meeting the challenges of the growing and changing needs of industry through delivering high quality technical education blended with training and research. The institute is approved by All India Council for Technical Education (AICTE), Accredited by NAAC 'A' Grade with a CGPA of 3.21, MHRDs NIRF-2019 Rank-180 and all the UG Engineering programmes are accredited by National Board of Accreditation (NBA) New Delhi.

ABOUT EEE DEPARTMENT:

The department of Electrical & Electronics Engineering (EEE) was established in the year 1994. The current intake in to UG program B.Tech (EEE) is 120 and PG program M.Tech (PE) is 18. The department is accredited by NBA under Tier - 1 in the year 2019. The department has dedicated and qualified faculty with 3 Professors, 6 Associate Professors, 21 Assistant Professors with 11 Doctorates, 04 Faculty members submitted PhD thesis and 08 pursuing Ph.D. in reputed Institutions/ Universities.

ABOUT THE FDP:

Theme of the FDP:

As electricity is majorly produced from either renewable sources or from sources that emit no greenhouse gases thus EVs help in maintain energy by shifting dependence from non-renewable resources to renewable resources

The electrification of the public and private transportation sector is anticipated to yield significant upgrade in commercial and environmental benefits by evolving definite energy saving mechanism, which lessen the dependence on imported fuel and reduce the hazardous greenhouse gas emission. This chronicle amendment shifts the transportation towards the incorporation of battery powered electric vehicles into the distribution system and persuade adverse impacts on overall power quality. The control algorithms and optimization techniques play an important role in improving power quality and dynamics of distribution system.

Tentative Topics:

1. Design and Implementation of 1-phase Micro Grid using Wind, Solar and Small Hydro
2. Battery management System for Evs
3. Wireless Charging of EVs
4. Advanced Power Electronics Applications in Aerospace, EV and Renewable Energy
5. Power System Optimization including Renewable Energy Sources
6. Advanced Control Systems and Power Quality Analysis

Resource persons from
NITs, Foreign Universities and Industry

Chief Patron

Dr. Alluri Murthy Raju
Chairman, KITS Warangal

Patrons

Capt. V. Lakshminantha Rao
Secretary & Correspondent, KITS Warangal
Honourable MP (Rajyasabha)

Sri P. Narayana Reddy
Treasurer, KITS Warangal

Chairman

Dr. K. Ashoka Reddy
Principal, KITS Warangal

Convener

Dr. C. Venkatesh
Professor & HOD, EEED, KITSW

Coordinators

Dr. V. Rajagopal, Professor, EEED, KITSW

Dr. Sabha Raj Arva, Associate Professor, SVNIT Surat

Co-ordinators

Dr. A. Madhukar Rao, Asst. Professor, EEED, KITSW,
9963902827/amr.eee@kitsw.ac.in

Dr. D. Rakesh Chandra, Asst. Professor, EEED,
KITSW, 9492442236 (drc.eee@kitsw.ac.in)

No Registration Fee

Registration Link

<https://forms.gle/ZzvD9ow7YrHiAits8>

Number of participants are limited to 150

Registrations will be closed by 5:00PM on
28-05-2020

Webinar links will be shared on 29-05-2020

Timings: 14:30 to 16:30

E-Certificate will be issued to participants
who will attend for all 05 days sessions.



One Week Faculty Development Program (Online)

on

Micro Grid, Electric Vehicles and Allied
Areas
(MGEVAA-2020)

1st- 5th June 2020

Organized by

Department of Electrical & Electronics
Engineering

KAKATIYA INSTITUTE OF TECHNOLOGY &
SCIENCE WARANGAL

(An AUTONOMOUS Institute under Kakatiya University-Warangal)

Opp: Yerragattu Gutta, Hasanparthy (M),
Warangal-506015 (Telangana), INDIA.

Tel (0870) 2564888, Fax: (0870) 2564320




















Website: www.kitsw.ac.in



FDP SCHEDULE

ISTE Sponsored One Week Faculty Development Program (Online) on Micro Grid, Electric Vehicles and Allied Areas 1st- 5th June 2020

Organized by
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

<div style="display: flex; justify-content: space-between; align-items: center;">  <div style="text-align: center;"> KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE WARANGAL <i>(An AUTONOMOUS Institute under Kakatiya University-Warangal)</i> Opp: Yerragattu Gutta, Hasanparthy (M), Warangal-506015, Telangana, INDIA. ISTE Sponsored One Week Faculty Development Program (Online) on Micro Grid, Electric Vehicles and Allied Areas 1st- 5th June 2020 Organized by DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING </div> <div style="text-align: right;">   </div> </div>				
Day1	Day2	Day3	Day4	Day5
 Dr. Mithun Bhaskar <i>Head, Model Based Product Engg, Tata Elxsi, Trivandrum</i>	 Dr. Sabha Raj Arya <i>Associate Professor & Coordinator SVNIT Surat</i>	 Dr. Papia Ray <i>Associate Professor VSSUT, Odisha</i>	 Dr. Ujwal Kalla <i>Associate Professor MANIT Bhoopal</i>	 Dr. Kalpana Ramesh Babu <i>Assistant Professor NIT Surathkal</i>
Topic EV Retrofitting	Topic DVR and DSC with Battery Charging	Topic SPA for fault location in a T and D network	Topic Design and Implementation of 1-phase MG using Wind, Solar and Small Hydro	Topic BMS for EVs
 Dr. Raj Kumar Garg <i>Associate Professor SLIE & T Punjab</i>	 Dr. Rajasekhar Reddy <i>Assistant Professor SVNIT Surat</i>	 Dr. S.N.Sharma <i>Professor SVNIT Surat</i>	 Dr. Surender Reddy <i>Assistant Professor Woosong University, South Korea</i>	 Dr. Sandeep Madishetti <i>Research Scientist, EPGCERI, NTU, Singapore</i>
Topic Recognition of PQ Disturbances	Topic Control And Synchroniza- tion of Grid Connected Multi-functional DGS	Topic Some Concepts in Advanced Control Systems	Topic Power System Optimization including RES	Topic APE Applications in Aerospace, EV and RE
<div style="display: flex; justify-content: space-between; align-items: center;">  <div style="text-align: center;"> Day5 Dr. Phaneendra Babu Bobba <i>Professor, GRIET Hyderabad</i> </div> <div style="text-align: right;"> Topic Wireless Charging of EVs </div> </div>				
Organizing Committee				
 Dr. K. Ashoka Reddy <i>Principal, KITSW Chairman</i>	 Dr. C. Venkatesh <i>Prof. & HOD, EEED, KITSW Convenor</i>	 Dr. V. Rajagopal <i>Professor, EEED, KITSW Coordinator</i>	 Dr. D. Rakesh Chandra <i>Asst. Professor, EEED, KITSW Co-Coordinator</i>	 Dr. A. Madhukar Rao <i>Asst. Professor, EEED, KITSW Co-Coordinator</i>

Message by Principal and Head of the EEE Department

ISTE Sponsored One Week Faculty Development Program (Online) on
Micro Grid, Electric Vehicles and Allied Areas

1st- 5th June 2020

Organized by
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Message by Principal, Professor K. Ashoka Reddy



Lock down motivating us to go for virtual (online) workshops. In the present scenario online FDPs are playing a key role for learning and knowledge transferring. I personally congratulate the department of EEE for taking the lead and conducting this type of online FDPs. I am conveying my sincere thanks to all keynote speakers for coming forward to share their knowledge in this platform and I am also congratulating all participants for registering in this workshop.

Message by HOD, EEE, Professor C.Venkatesh



In the coming days Electric Vehicles (EV) will play a vital role and at present researchers are also working on efficiency and feasibility of EVs. In the present day environment Microgrid is a buzz word which can also incorporate Renewable Energy Sources. I am congratulating coordinators for the initiation of the workshop and participants for showing interest towards this workshop.

Message by Coordinators & Co - Coordinators

**Professor V. Rajagopal, Dr. Sabha Raj Arya,
Dr. D. Rakesh Chandra & Dr. A. Madhukar Rao**



Micro Grid, Electric Vehicles and Allied Areas (MGEVAA – 20) is an online FDP conducted from 1st– 5th June 2020. There are 760 registrations for this FDP, out of that we could shortlist only 200 participants. The Participants include faculty and research scholars from prestigious Institutes like IISC and IITs and NITs and various good engineering Colleges across the country from 13 different states.

We had 11 speakers out of the 09 are national speakers and 02 are international speakers (out of that two are from industries). First day Dr. Mithun Bhaskar, MBD Head, Model Based Product Engg, Tata Elxsi, Trivandrum spoke on “xEV Autonomy, Retrofitting” and Dr. Raj Kumar Garg, Associate Professor, Sant Longowal Institute of Engineering & Technology, Punjab spoke on “Recognition of Power Quality Disturbances”. Second day Dr. Sabha Raj Arya & Coordinator, Associate Professor, SVNIT Surat spoke on “Control algorithms for Custom Power Devices and its Applications” and Dr. Rajasekhar Reddy, Assistant Professor, SVNIT Surat spoke on “Multifunctional Control and Grid Synchronization of VSI in Distributed Generation Systems”. Third day Dr. Papia Ray, Associate Professor, Veer Surendra Sai University of Technology, Odisha spoke on “Signal Processing Application for Fault Location in a Transmission and Distribution Network” and Dr. S.N.Sharma, Professor, SVNIT Surat, Gujarat spoke on “Four Problems in the Hamiltonian Jacobi-Bellman Equation”. Fourth day Dr. Ujwal Kalla, Associate Professor, MANIT Bhoopal spoke on “Design and Implementation of 1-phase Micro Grid using Wind, Solar and Small Hydro” and Dr. Surender Reddy, Associate Professor Woosong University, South Korea spoke on “Optimal Operation of Power System with Renewable Energy Sources” and Last and fifth day Dr. Phaneendra Babu Bobba, Professor, GRIET Hyderabad spoke on “Wireless Charging of EVs”, Dr. Kalpana Ramesh Babu, Assistant Professor, NIT Surathkal spoke on “Battery management System for EVs” and Dr. Sandeep Madishetti, Research Scientist, EPGC, ERI@NTU, Singapore spoke on “Advanced Power Electronics Applications in Aerospace, EV and Renewable Energy”.

I thank all the resource persons for agreeing and giving a lecture at this FDP. I thank Management, Principal, Prof. Ashoka Reddy and HOD EEE, Prof. C. Venkatesh for encouraging and guiding us to conduct this FDP. I also thank the Faculty of KITS Warangal, faculty and research scholars of other colleges attending this workshop. I also thank Mr. Ajith, Assistant Professor and Mr Suresh Programmer DSL Lab for helping us for successful completion of FDP.

SPEAKERS BIODATA

Dr. Mithun Bhaskar:



Dr. Bhaskar is an influential engineering leader with a Ph.D in Computational Intelligence, known for leveraging emerging & disruptive technologies, build Centre of Excellences (COE's) to execute projects for products in multiple domains. 14 years of professional experience in leading MNCs and other organizations, with skills in creating physics based models, integrating testing with design, production code generation, rapid control prototyping and automating processes to accelerate development. Invited speaker at international and national seminars, author of technical papers and member of industry bodies such as IEEE, BIS and NIST.

Dr Bhaskar has built and managed 300+ world class Model based design embedded engineers at Tata Elxsi in multi disciplinary (Automotive, Rail, Avionics, Consumer Electronics, Medical Electronics & Robotics), multi geography BU's, delivering advanced embedded product design engineering services. The team has scaled up 525% with proportionate engagements in few years. He received IEEE MGA young Professionals Achievement Award for demonstrating leadership in 2012 and has submitted 5 patents.

Dr. Raj Kumar Garg :



Dr. Raj Kumar received the B.E. degree in Electronics and Instrumentation Engineering fr

om Punjabi University, Patiala, India, in 1994, the M.Tech degree in Instrumentation and Control Engineering from Punjab Technical University, Jalandhar, India, in 2005, and the Ph.D. degree from Indian Institute of Technology Delhi, New Delhi, India, in 2016.

He is currently an Associate Professor with the Electrical and Instrumentation Engineering Department, Sant Longowal Institute of Engineering and Technology, Longowal, India. The institute was established by the Ministry Of Human Resource and Development (MHRD), Govt. of India. He has 20 Years of Teaching and 05 Years of industrial experience. His current research interests include power quality, digital signal processing, and process control systems.

Dr. Sabha Raj Arya:



Dr. Sabha Raj Arya received Bachelor of Engineering degree in Electrical Engineering from Government Engineering College Jabalpur, in 2002, Master of Technology in Power Electronics from Motilal National Institute of Technology, Allahabad, in 2004 and Ph.D. degree in Electrical Engineering from Indian Institute of Technology (I.I.T) Delhi, New Delhi, India, in 2014. He is joined as Assistant Professor, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology, Surat. January 2019, he is promoted as Associate Professor in same institute. His fields of interest include Power electronics, power quality, design of power filters and distributed power generation.

He received Two National Awards namely INAE Young Engineer Award from Indian National Academy of Engineering, POSOCO Power System Award from Power Grid Corporation of India in the year of 2014 for his research work. He is also received Amit Garg Memorial Research Award-2014 from I.I.T Delhi from the high impact publication in a quality journal during the session 2013-2014. At present, he has published more than *hundred* research paper in internal national Journals and conferences in field of electrical power quality.

He is also serving as an Associate Editor for the *IET (U.K.) Renewable Power Generation*.

Dr. Rajasekhar Reddy:



Dr. Rajasekhar Reddy received the Bachelor's degree in electrical and electronics engineering from the Jawaharlal Nehru Technological University Hyderabad, India, in 2006, and the Ph.D. degree in electrical engineering from the Indian Institute of Technology Delhi, New Delhi, India, in 2014. From Nov. 2013 to Oct. 2014, he was with the department of electrical and computer engineering department, National University of Singapore (NUS), Singapore, where he worked as post-doctoral research fellow. From Dec. 2014 to Nov. 2019, he was with the department of electrical and computer engineering department, Khalifa University of Science and Technology, Abu Dhabi, where he worked as post-doctoral research associate. Currently, he is with the Electrical Engineering Department, SVNIT, Surat, working as an assistant professor.

His research work has been published in various high quality academic journals and international conference proceedings. His research interests include voltage and frequency control of self-excited induction generators, power electronics applications in renewable energy systems, distributed power generation, power quality, and control of custom power devices and wireless power charging.

Dr. Papia Ray:



Dr. Papia Ray is an Associate Professor at the Department of Electrical Engineering, Veer Surendra Sai University of Technology, Odisha, India. She completed her Ph.D. in the area of power system engineering at the Indian Institute of Technology, Delhi. Her current research area includes Power system protection, Power quality, Wide area measurement systems and application of soft computing techniques in power system protection. She is a senior member of IEEE, Life Member of Indian Society for Technical Education and Member of Institution of Engineer's India. She has published numerous papers in various international journals and conferences. She has produced one Ph.D. Recently She has written a book titled "Microgrid: Operation, Control, Monitoring and Protection", Springer Publisher. She is the recipient of Young Scientist Award from Department of Science and Technology, New Delhi in the year 2015.

Dr. S.N.Sharma:



B.E. Electrical Engineering, Govt. Engineering College Rewa, M.P., India, M.Tech. Control Systems (Electrical Engineering), Institute of Technology, Banaras Hindu University (Presently IIT (BHU)), UP, India. Ph.D. Volterra and stochastic system theory, the University of Delhi, Delhi, India.

From 2000 to 2009 he worked with NSIT New Delhi (An Autonomous Institution of Govt. of NCT of Delhi) and From 2009 to till date he is with SVNIT Surat.

Visiting academic appointment under the INSA

(1) Department of Systems and Control, Jozef Stefan Institute, Republic of Slovenia,
May 20-June 20, 2018.

Known for and selected contributions:

- (1) A stochastic system, 'The Sharma-Parthasarathy stochastic two-body problem'.
- (2) Higher-order filtering: We have developed higher-order Kushner filter and the result was published in International Journal of Control and Automatica.
- (3) Introduction of Stratonovich differential into non-linear dynamic circuits:

Current research directions are the following:

- (1) Stochastic processes, dynamical systems and non-linear filtering
- (2) Multivariable Systems, Relative Normalized Gain Array, IMC Controllers
- (3) Volterra systems and IMC Volterra Controllers, specifically extending the notion of IMC controllers for non-linear systems using Carleman embedding

(4) Developing new perspectives in filtering and control

He is Reviewer of Many Renowned Journals

He authored many quality Journals

Book Chapters

Two articles (single Authored), two (double authored)

06 PhD thesis guided and guiding 06 PhD Students

Funding acquired (A) CSIR research Project (B) An MHRD Pedagogy project

Dr. Ujwal Kalla:



Dr. Ujjwal Kumar Kalla

Ph.D. (in Electrical Engg.) IIT Delhi, M.Tech. Power elect., Elect. m/c & Drives (IIT Delhi) ,

Associate Editor of IET RPG, Fellow IETE (India), Fellow IEI (India), Senior Member IEEE

Total Teaching Experience 18 Years

Currently working as Associate Professor, Department of Electrical Engineering, Maulana Azad National Institute of Technology, Bhopal (India) An Institute of National Importance, Govt. of India

Former Project Director, (April 2015 to February 2020)

Ceramic Electrical Research Development Center, Bikaner (Rajasthan) India (An Autonomous Organization of Govt. of Rajasthan)

An N.A.B.L., Govt. of India, Accredited organization & **Former Head**, Department of Electrical Engineering, Govt. Engineering College Bikaner, (Rajasthan), India

National Awards received during M.Tech. & Ph.D. for excellence in academics:

1. National Award For Best M. Tech. Thesis of I.S.T.E. in Electrical and Electronics Engineering 2010". (**All India First Prize**) (cash award Rupees 10,000 and a certificate of Appreciation)
2. POSOCO Power System Award (in Doctoral Category) from FITT, IITD & POSOCO. (cash award Rupees 60,000 and a certificate of Appreciation)
3. GRIDTECH 2015 award of Power Grid and Ministry of Power (**All India First Prize**) in During PhD.

(cash award Rupees 1 Lac and a certificate of Appreciation)

- ☐ **Patents Filled:-** 4 patents
- ☐ **M.Tech Thesis Guided:** 35 (Awarded), 8 Under Progress
- ☐ **Ph.D. Guided :** 1 (submitted) , 4 (In Progress),
- ☐ **Google scholar Citations= 547, h - index papers=15, I 10-index papers =16,**

Research Projects:

1. **PI of Research Project** Titled :- "High performance PFC based LED Drivers working under Stringent AC Supply". Granted by C.P.R.I., Bangalore, G.O.I., **Grant Amount Rs 34.76 Lakh.** (completed in June 2019)
2. **PI of Research Project** Titled :- Design and Implementation of single phase Microgrid using renewable energy sources, Under Extra Mural Research Funding, of Central DST, G.O.I., **Grant Amount Rs 62.99 lakhs.** (in progress)

3. **PI of Research Project** Titled:- Design and Development of Electronic Load Controller for voltage and frequency control of Self Excited Induction Generators (SEIG) **Grant Amount 2.40 Lac.**

4. **CO-PI of Research Project** Titled:- Design, Development and Analysis of DC/DC converter Schemes using new types of efficient switching devices and magnetic materials for Improved maximum Power Extraction system from Solar PV Generation Systems , **Grant Amount 2.40 Lac.**

Dr. Surender Reddy:



Dr. Surender Reddy received the Ph.D. degree in electrical engineering from Indian Institute of Technology, New Delhi, India, in 2013. He was a Postdoctoral Researcher at Howard University, Washington, DC, USA, from 2013 to 2014. He is currently working as an Associate Professor in the Department of Railroad and Electrical Engineering, Woosong University, Daejeon, Republic of Korea. He published 100+ international journal papers, and 20+ international conference papers.

His current research interests include power system restructuring issues, ancillary service pricing, real and reactive power pricing, congestion management, and market clearing, including renewable energy sources, demand response, smart grid development with integration of wind and solar photovoltaic energy sources, battery storage and electric vehicles, artificial intelligence applications in power systems, and power system analysis and optimization. He received Distinguished Researcher Award from Woosong University Educational Foundation, Republic of Korea in 2016, and POSOCO Power System Award (PPSA), India in 2013. He is a Member of IEEE and IEEE Power and Energy Society.

Dr. Phaneendra Babu Bobba:



Dr. Phaneendra holds a Ph. D. from Indian Institute of Technology (IIT) Delhi India. He is presently working as a Professor in the Department of EEE at GRIET, Hyderabad, India. Prior to joining GRIET he worked as Assistant Professor in Shiv Nadar University. Dr. Phaneendra has published 35 publications in National and International Journals and Conferences. He is working as lead technical consultant to Asthra Projects and Master Pcb Pvt. Ltd. He is working on consultancy projects worth of 58 lakhs.

Research Interests: Electric Vehicles, Hybrid electric vehicles and Plug-in hybrid electric vehicles. • Hybrid Energy storage systems (Battery and Supercapacitor combination) • Power management systems in Electric vehicles. • Electric Drives, Power Electronics, DSP based control of Drives • Dynamic Wireless charging in supercapacitor based EVs. • Design of Wireless power transfer systems for Mobile and Medical Applications. • V2G and G2V communication / Smart Grid.

Dr. Kalpana Ramesh Babu:



Dr. R Kalpana received B.Tech degree in Electrical and Electronics Engineering from Madras University, Chennai, Tamilnadu, India, in 1998, M.E. degree from Anna University, Chennai, Tamilnadu India, in 2000, and Ph. D degree from Indian Institute of Technology Delhi, India, in 2012. She is currently working as Assistant Professor at Department of Electrical Engineering, National Institute of Technology, Surathkal, India. She is a senior IEEE Member since 2018. She has publications in more than 20 in IEEE Journals and 50 National/International Conferences and filed 3 Indian patents. Her fields of interest are improved power quality converters, Renewable energy systems and Battery management systems.

Dr. Sandeep Madishetti :



Dr. Sandeep Madishetti Received the PhD degree from IIT Delhi, in 2015, the M.Tech. degree in Power Electronics and Electrical Drives from S.V.NIT, Surat, in 2009, and the B.Tech. degree in Electrical and Electronics Engineering from Anurag Engineering College, Kodad, in 2007. After his completion of doctoral studies, he worked for two years at Rolls Royce @ Nanyang Technical University Corporate Lab, Singapore, as a Research Fellow. He is currently working as Research Scientist with Experimental Power Grid Center (EPGC), Singapore since 2017. Earlier EPGC was a Research Institute under A*STAR, Singapore. In September 2019 EPGC merged with Energy Research Institute @ Nanyang Technological University (ERIAN).

His research interests include power electronics, electrical drives, power quality, more electric aircraft, distributed energy generation, renewable energy intermittency, smart grid, wide-bandgap devices, high power density converters, embedded control systems. He received POSOCO Power System Award (PPSA) 2015. His PhD thesis work stood top ten in Doctorial Category.

DATE: 01-06-2020

TIME: 2.20 – 2.30pm

SESSION: I

TITLE: Inaugural

- Principal, Professor K. Ashoka Reddy has given a speech about the role of FDPs to improve technical skills and knowledge.

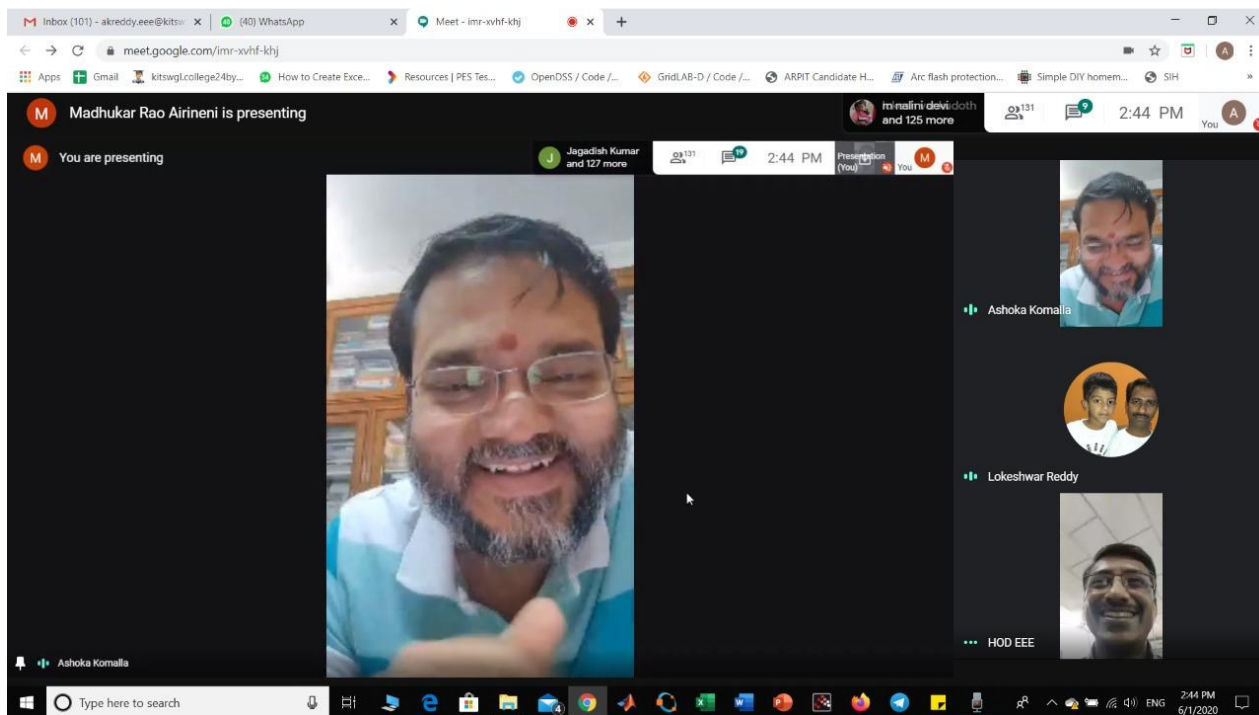


Photo I: Prof. K. Ashoka Reddy, while speaking in the inaugural

DATE: 01-06-2020

TIME: 2.30 – 3.30pm

SESSION: 1

TITLE: EV Retrofitting

RESOURCE PERSON: Dr. Mithun Bhaskar, *Head, Model Based Product Engg, Tata Elxsi, Trivandrum*

REPORT: In this session Dr. Mithun Bhaskar delivered the following points in his lecture.

- ☐ Vehicle supervisory control, Battery management, mathematical modeling and system modeling.
- ☐ Discussed Challenges in retrofitting
- ☐ Specifications of Electric vehicle
- ☐ Mechanical design and packaging and also vehicle dynamics.
- ☐ In Electrical design component selection, compatibility and controllability.
- ☐ Safety constraints such as battery cooling, motor cooling and emergency cutoff.
- ☐ Importance of Model based System Engineering
- ☐ Description of Vehicle to Everything (V2X) concept.

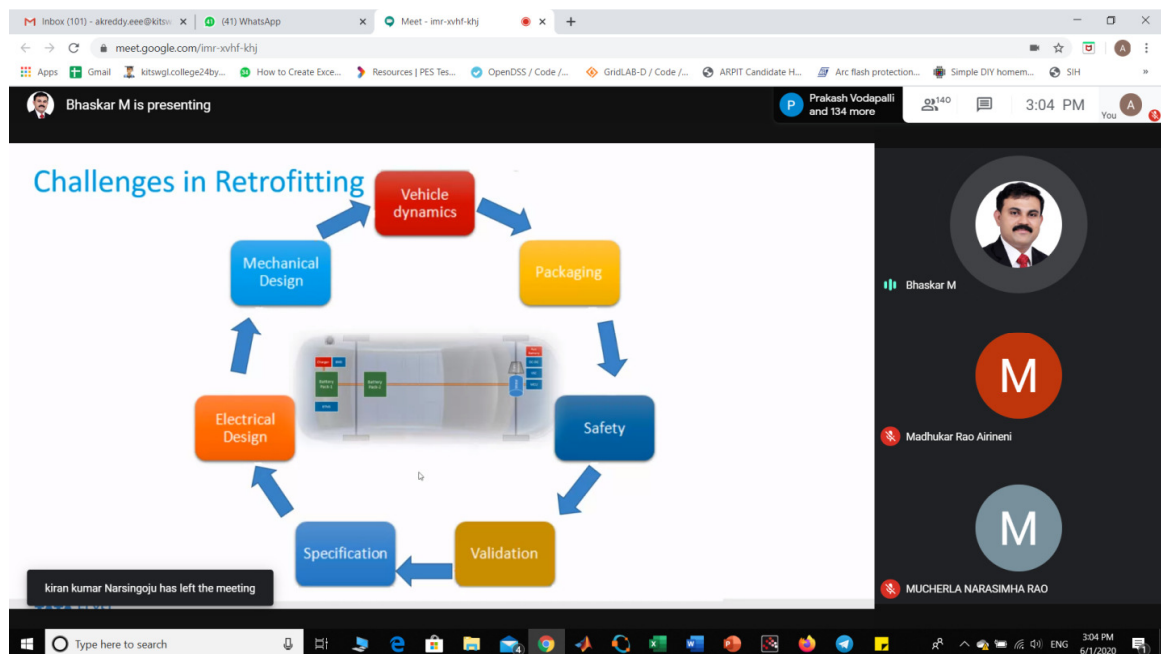
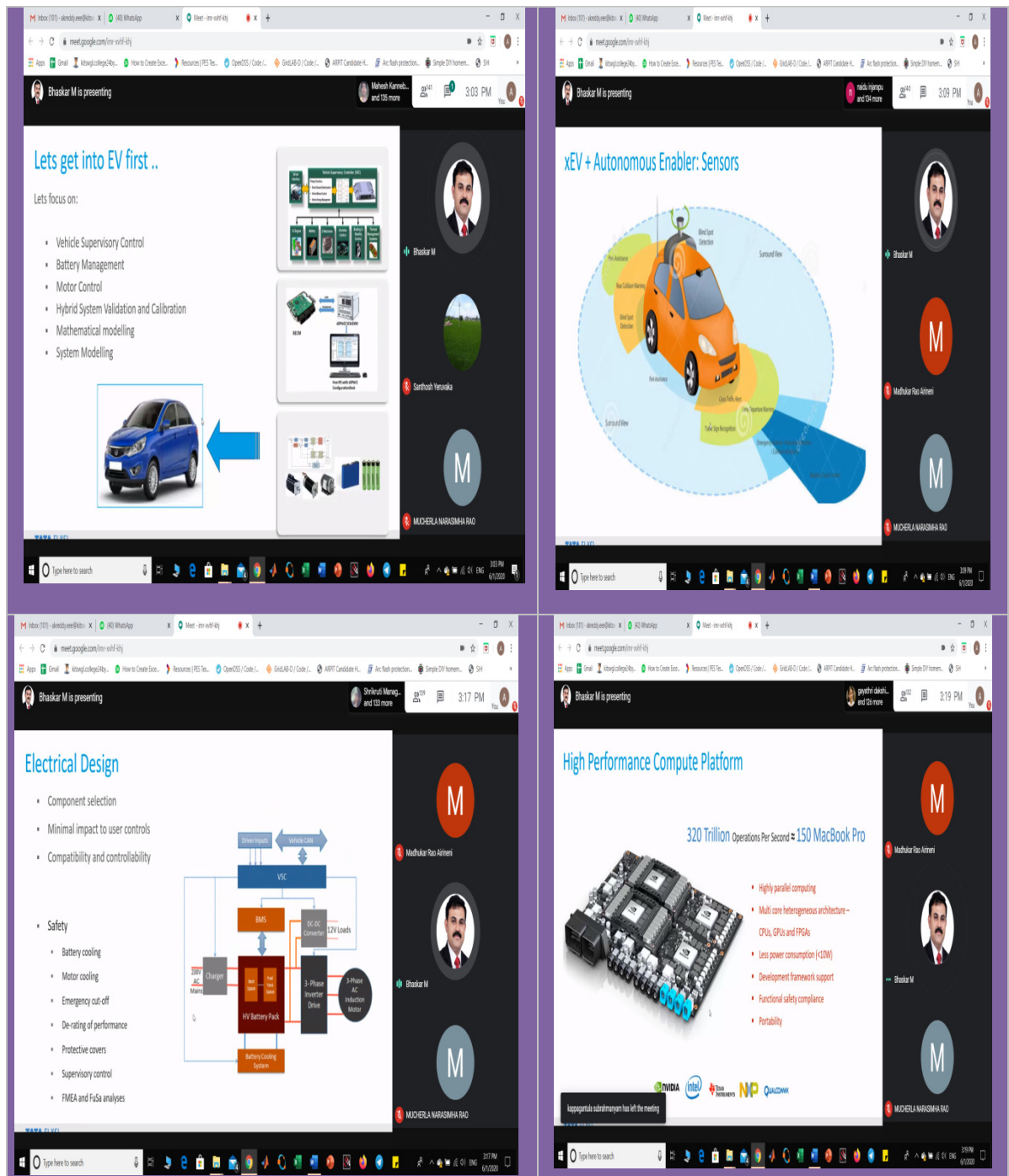


Photo 1: Dr. Mithun Bhaskar, while delivering the Lecture



Dr. Mithun Bhaskar session screen shots

DATE: 01-06-2020
TIME: 3.30 – 4.30pm

SESSION: 2

TITLE: Recognition of Power Quality Disturbances

RESOURCE PERSON: Dr. Raj Kumar Garg, Associate Professor, Sant Longowal Institute of Engineering & Technology, Punjab

REPORT: In this session Dr. Raj Kumar Garg delivered the following points in his lecture.

- ☐ Definition of power quality and the significance of power quality
- ☐ Discussion on various power quality disturbances
- ☐ Classification of power quality disturbances
- ☐ What is the necessity of detecting power quality disturbances?
- ☐ Discussed power quality monitoring phenomena.
- ☐ Detailed discussion on stock well transformation.
- ☐ Purpose of S transform to analyze the sag, swell and harmonic disturbances.
- ☐ Flow chart for PQ disturbances classification.

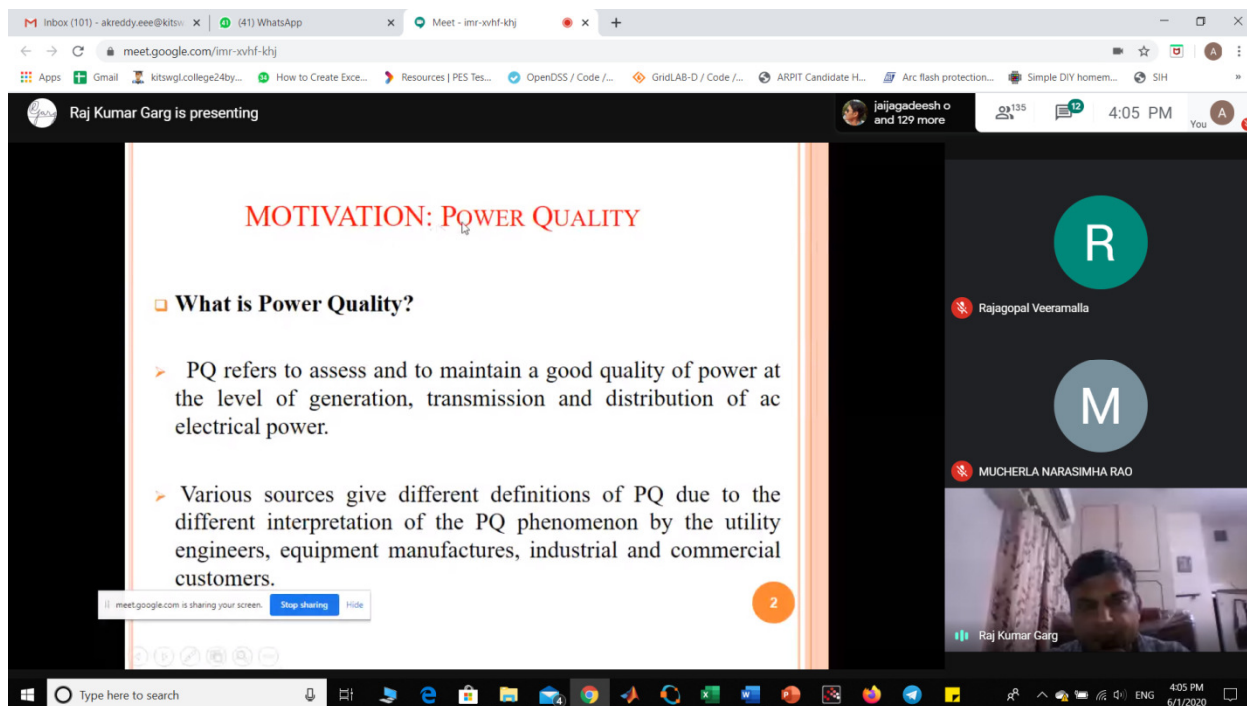
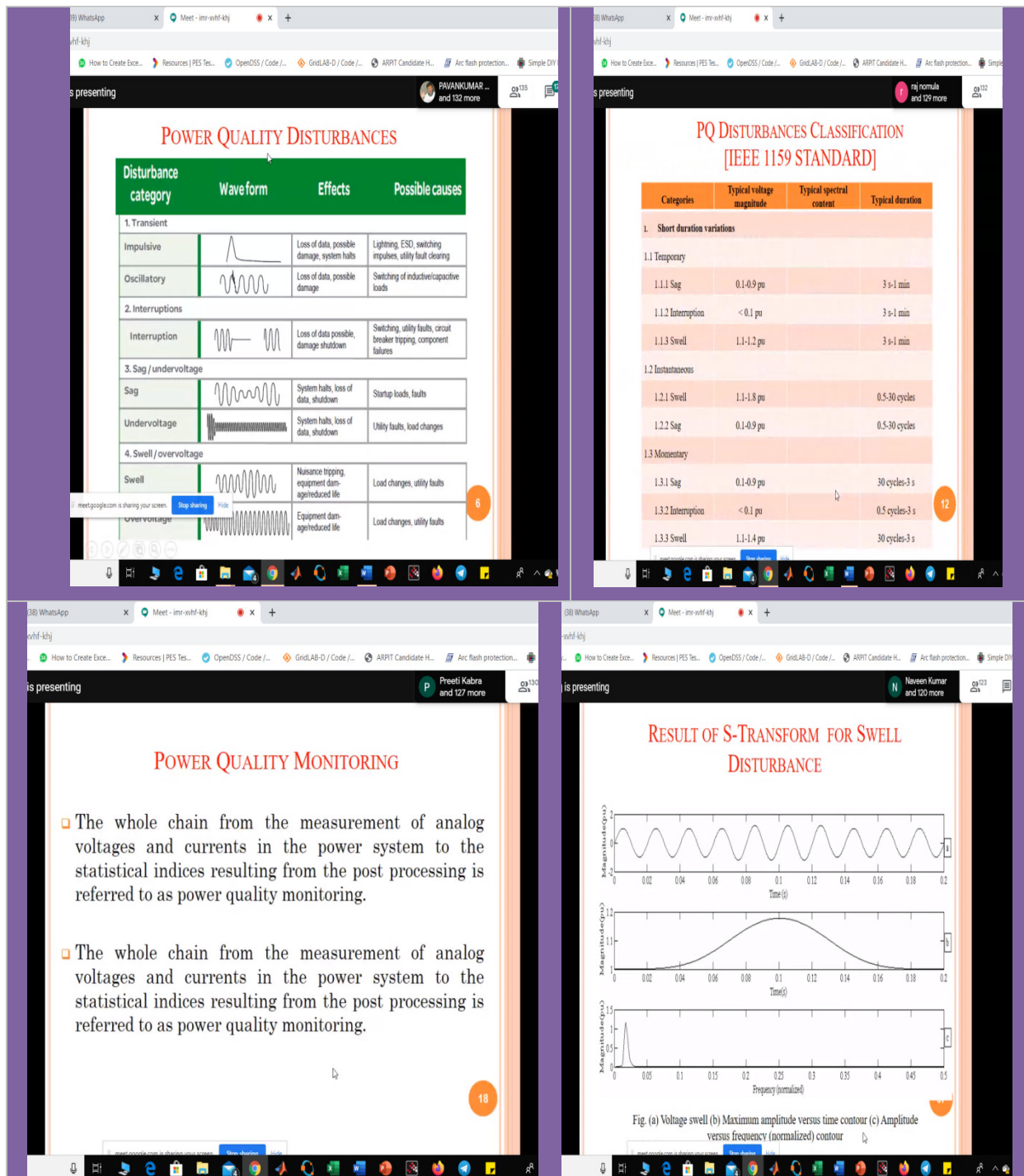


Photo 2: Dr. Raj Kumar Garg, while delivering the Lecture



Dr. Raj Kumar Garg session screen shots

DATE: 02-06-2020

TIME: 2.30 – 3.30pm

SESSION: 3

TITLE: Dynamic Voltage Restorer, Distribution Static Compensator with Battery Charging

RESOURCE PERSON: Dr. Sabha Raj Arya, Associate Professor, SVNIT Surat

REPORT: In this session Dr. Sabha Raj Arya delivered the following points in his lecture.

- ☐ Description of three phase three wire and three phase four wire DSTATCOM
- ☐ How to select DC bus capacitor and voltage
- ☐ Explanation of designing ripple filter
- ☐ Adaptive neural network based control algorithm in three phase four wire system.
- ☐ Hard ware implementation of DSTATCOM has been demonstrated.
- ☐ Different topologies and methods of operation of DVR have been discussed.
- ☐ Simulation results of DVR with different PLLs have been analyzed.
- ☐ Experimental setup of DVR with three phase VSC based topology has been demonstrated.

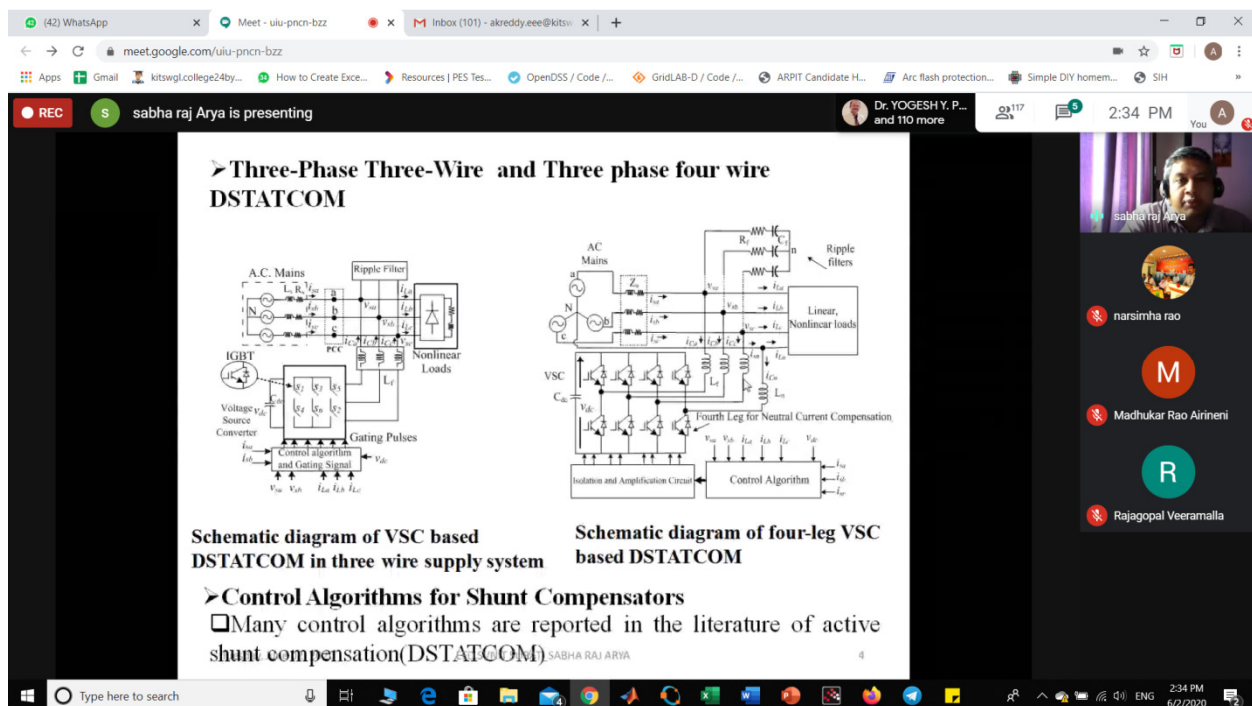


Photo 3: Dr. Sabha Raj Arya, while delivering the Lecture

Dr. Sabha Raj Arya is presenting

DSTATCOM

The application of shunt active filters in the distribution system which is used for mitigation of **current related power quality problems**.

Function of DSTATCOM

- Reactive power compensation,
- Harmonic Suppression,
- Load balancing
- Neutral current compensation

Mode of operation

- Power Factor Correction
- Zero Voltage Regulation

Configurations of Three-Phase DSTATCOMs

Configurations of Three-Phase DSTATCOMs

Dr. Sabha Raj Arya is presenting

Design of DSTATCOM

Selection of VSC

Selection of VSC rating in three-phase three-wire system depends upon the compensation requirement such as reactive power, harmonics and load balancing.

For a considered load of 35kVA (0.8 lagging), the rating of the VSC for the reactive power compensation harmonics elimination is found to be 25 kVA (approximate 15% higher than the reactive power from rated value).

Dr. Sabha Raj Arya is presenting

Dr. Sabha Raj Arya is presenting

Fig. 6. Waveforms and harmonic spectra of (a) PCC voltage of phase 'a' (b) supply current of phase 'a' in PFC mode (c) load current of phase 'a' in PFC mode

Dr. Sabha Raj Arya session screen shots

SESSION: 4

TITLE: Control and Synchronization of Grid Connected Multi-functional Distributed Generation System

RESOURCE PERSON: Dr. Rajasekhar Reddy, Assistant Professor, SVNIT Surat

REPORT: In this session Dr. Rajasekhar Reddy delivered the following points in his lecture.

- ☐ DG system with shunt and series compensation capabilities.
- ☐ Schematic diagram of single phase DG has been explained.
- ☐ Multiple ANC based filters structure has been discussed.
- ☐ Concept of compensating currents and estimation of compensating forces has been discussed.
- ☐ Hard ware implementation of DG inverter system has been demonstrated.
- ☐ Concept of multiple DG system s has been explained with reference to the schematic diagram.
- ☐ Discussion on multiple DG inverters with Ancillary services.
- ☐ Overall control diagram of single phase SPV- UAPF system has been discussed.

Low Voltage Ride Through Currents

During low voltage condition, the control algorithm injects reactive current to aid grid recovery and reduces the active the active power generation to accommodate the LVRT current. Further, the control algorithm sets the compensating currents to zero. The magnitude of reactive current (I_Q^*) to be injected by the inverters is computed based on the PCC voltage magnitude and according to grid codes.

$$I_Q^* = \begin{cases} 0, & \text{if } V_m \geq 0.9. \\ \frac{9}{4} \times (0.9 - V_m) \times \sqrt{2} I_r, & \text{if } 0.5 < V_m < 0.9. \\ 0.9 \times \sqrt{2} I_r, & \text{if } V_m \leq 0.5. \end{cases} \quad (16)$$

where V_m is PCC voltage in p.u. and I_r is inverter rated current.

$$I_{Plim} = \sqrt{(\sqrt{2} I_r)^2 - I_Q^2}. \quad (17)$$
$$I_P^*(k) = \begin{cases} I_P^*(k), & \text{if } I_{Plim}(k) \geq I_P^*(k). \\ I_{Plim}(k), & \text{if } I_{Plim}(k) < I_P^*(k). \end{cases} \quad (18)$$
$$i_{Q1}^* = -I_{Q1}^* \times \cos \omega_1 t \quad i_{Q2}^* = -I_{Q2}^* \times \cos \omega_1 t \quad (19)$$

where $\cos \omega_1 t$ is the quadrature phase template of the fundamental PCC voltage.

Photo 4: Dr. Rajasekhar Reddy, while delivering the Lecture

System Configuration

Figure 1: Schematic of single-phase DG system.

Extraction of Harmonic and Reactive Currents

Fig. 3 depicts the block diagram of adaptive noise cancellation (ANC) filter where it is used to extract the fundamental component of load current. The ANC filter has two inputs, namely phase angle (θ) and i_l . To obtain the θ of fundamental PCC voltage (v_{pcc}), a single-phase PLL is used and the output of PLL is applied to the ANC filter as shown in Fig. 3.

Figure 3: Block diagram of ANC filter.

The currents $i_{lf(p)}$ and $i_{lf(q)}$ are obtained by multiplying W_{pf} and W_{qf} with $\cos \theta$ and $\sin \theta$, respectively. Therefore, the weights W_{pf} and W_{qf} in the ANC filter represent the amplitudes of $i_{lf(p)}$ and $i_{lf(q)}$, respectively.

Estimation of Compensating factors

Figure 6: Flow chart for estimating compensating factors.

Hardware Implementation

Figure 7: Experimental setup of grid-tied DG inverter system

Dr. Rajasekhara Reddy session screen shots

SESSION: 5

TITLE: Signal processing application for fault location in a transmission and distribution network

RESOURCE PERSON: Dr. Papia Ray, Associate Professor, Veer Surendra Sai University of Technology, Odisha

REPORT: In this session Dr. Papia Ray delivered the following points in his lecture.

- ☐ Concept of Impedance measurement based method and travelling wave phenomenon base method.
- ☐ Usage of AI based methods for fault classification and location.
- ☐ Fault location identification on transmission and distribution line.
- ☐ Schematic diagram of entire protection scheme for fault classification and identification.
- ☐ Fault analysis procedure with Wavelet transforms has been discussed.
- ☐ Concept of Forward feature selection method has been discussed.
- ☐ Fundamentals of Genetic Algorithm (GA) and its usage for feature selection has been discussed.
- ☐ Fault location in a series compensated transmission line has been discussed.

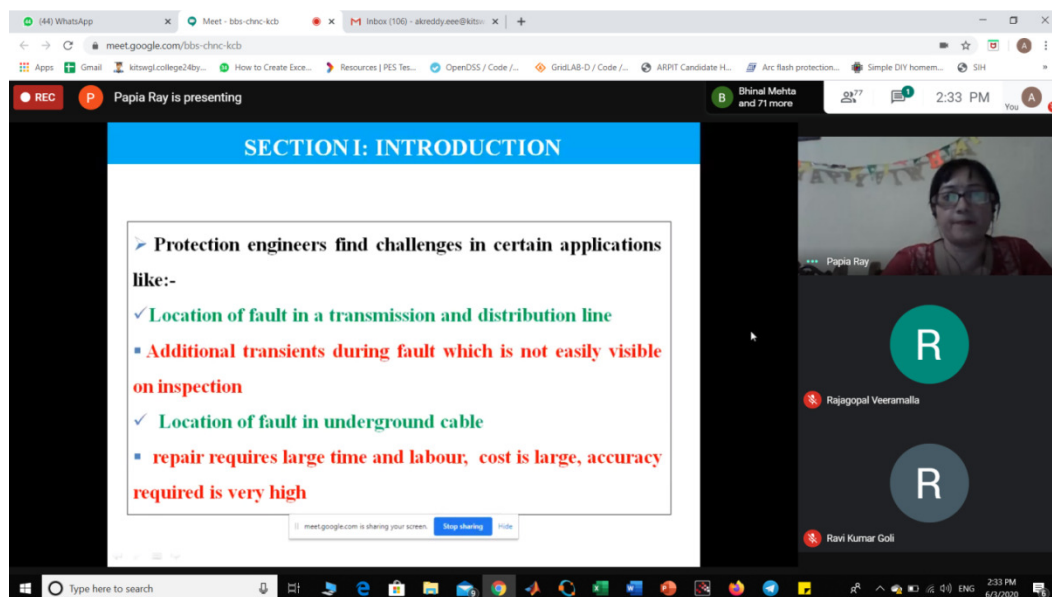


Photo 5: Dr. Papia Ray, while delivering the Lecture

Meet - bbs-chnc-kcb

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Papia Ray is presenting

SECTION I: INTRODUCTION

IMPORTANCE OF THE WORK IN THE PRESENT SCENARIO

- Fault must be located properly, otherwise the whole line has to be inspected by the maintenance crew to find the exact location of the fault.
- Proper location identifies the part of the transmission line that has been faulted and the patrolling vehicle of the transmission O&M agency can reach the spot at which the fault has occurred and take up repair/ correction activities without wasting any further time.
- Tripping of lines in an important transmission corridor can lead to reduced levels of power from one part of the country to the other (from a power-surplus area to a power starved area) and if important transmission lines trip, hunting of system, collapse of part or whole of the grid is also possible. So healthiness of lines and reduced outage of lines is very important and this is where fault locator plays a very important role.
- Proper rectification, evaluation of control strategies

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2:30 PM

Meet - bbs-chnc-kcb

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Papia Ray is presenting

SECTION - I

TECHNIQUES FOR TRANSMISSION LINE

- Impedance measurement based method
- Travelling wave phenomenon based method
- Artificial Intelligence (AI) based method/Statistical based approaches

For quite a few years, AI based methods are being used for fault classification and location.

- Three major AI based techniques have been widely used in the power industry
 - Expert system technique
 - Artificial neural network (ANN) based technique/SVM based method/Signal processing technique based method
 - Fuzzy logic system

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Papia Ray is presenting

SECTION I: HYBRID AI BASED METHOD

ENTIRE PROTECTION SCHEME FOR FAULT CLASSIFICATION & LOCATION

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2:43 PM

Meet - bbs-chnc-kcb

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Papia Ray is presenting

SECTION I: HYBRID AI BASED METHOD

FLOWCHART FOR PROPOSED FAULT LOCATION METHOD

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2:43 PM

Dr. Papia Ray session screen shots

SESSION: 6

TITLE: Some Concepts in Advanced Control Systems

RESOURCE PERSON: Dr. S.N.Sharma, Professor, SVNIT Surat , Gujarat

REPORT: In this session Dr. S.N.Sharma delivered the following points in his lecture.

- ☐ First order problem description based on the differential equation.
- ☐ First problem in the Hamiltonian Jacobi Bellman equation.
- ☐ The constraints of State, Control and the performance measure are achieved using the bounds or inequalities.
- ☐ Four problems in the Hamiltonian Jacobi Bellman equation.
- ☐ The first step in using Hamiltonian Jacobi Bellman equation is to determine the admissible control.
- ☐ The control signal inequality implies the state constraint as well as performance measure inequality.
- ☐ Concept of Energy inequality has been discussed.

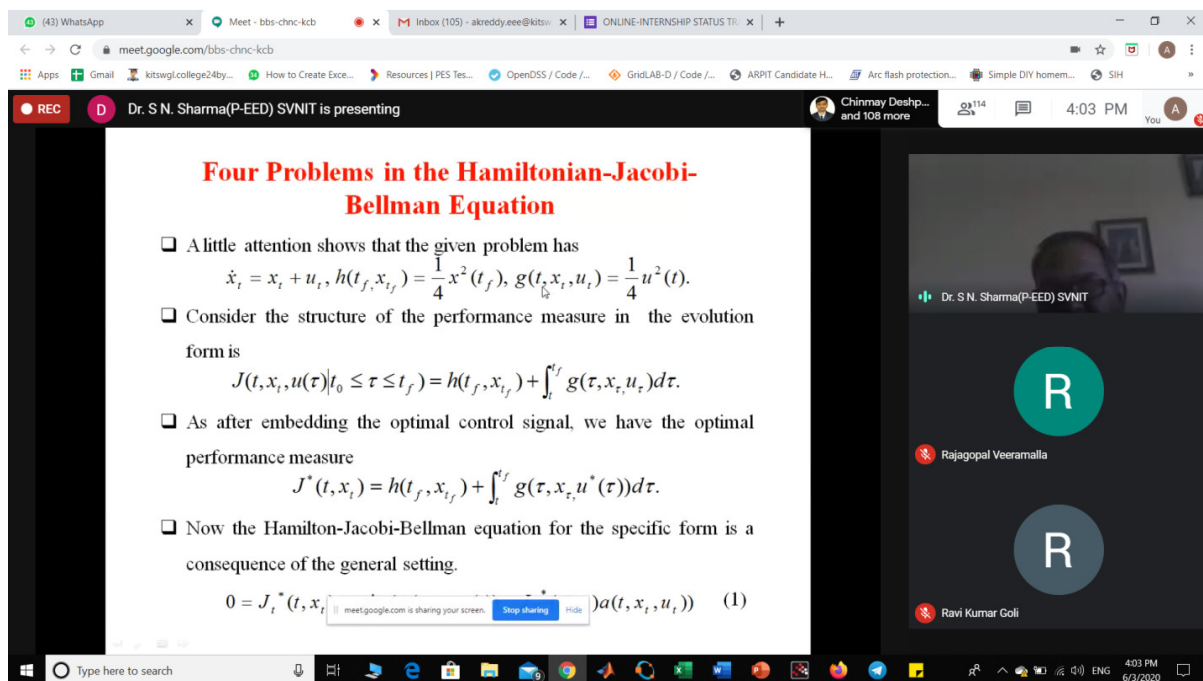
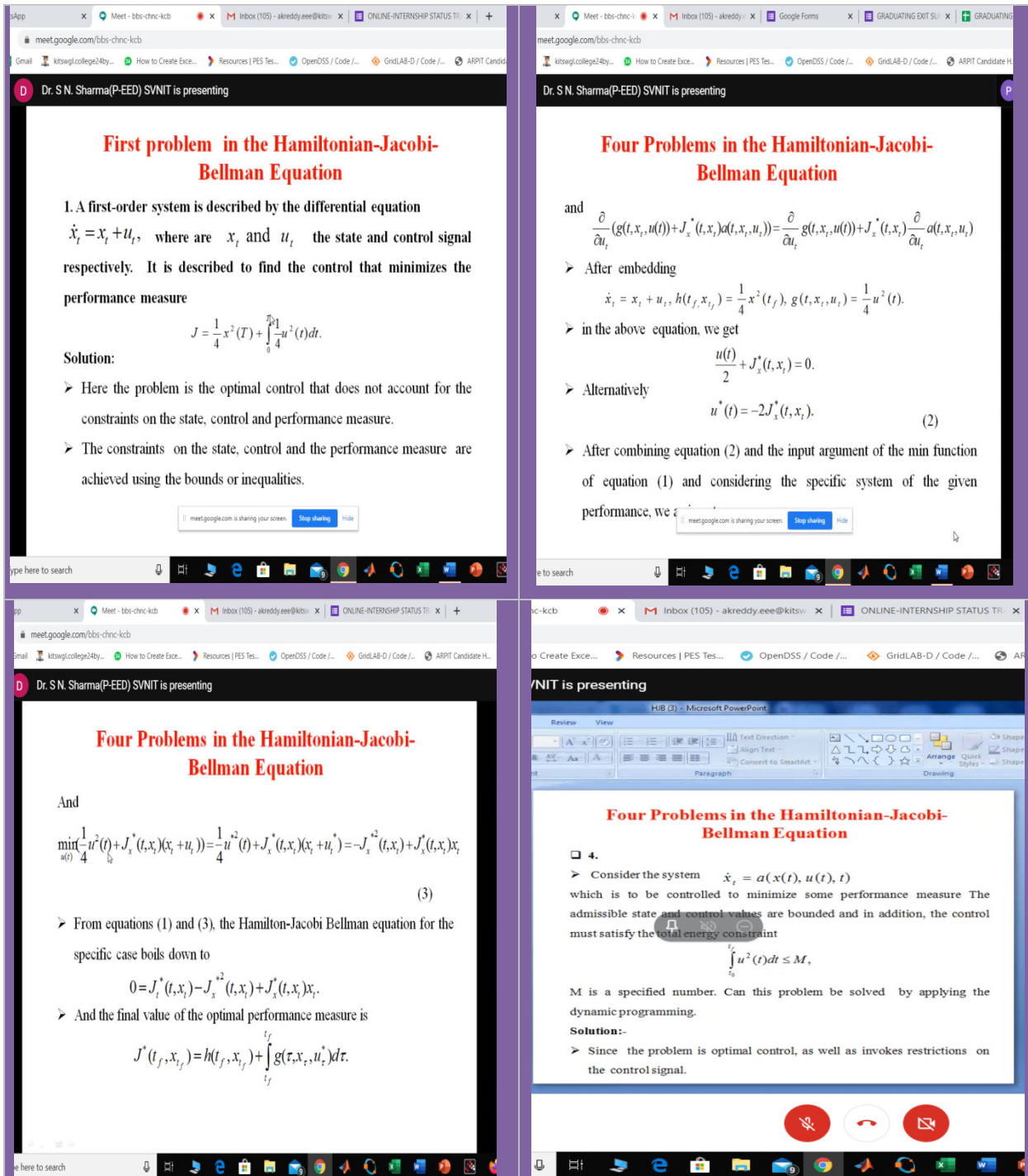


Photo 6: Dr. S.N.Sharma, while delivering the Lecture



Dr. S.N. Sharma session screen shots

SESSION: 7

TITLE: Design and Implementation of 1-phase Micro Grid using Wind, Solar and Small Hydro

RESOURCE PERSON: Dr. Ujwal Kalla, Associate Professor, MANIT Bhopal

REPORT: In this session Dr. Ujwal Kalla delivered the following points in his lecture.

- ☐ Fundamental concepts of Microgrid discussed.
- ☐ Hard ware implementation of developed prototype has been explained.
- ☐ Concept of non linear load consisting of a bridge rectifier, resistive load bank and inductive load bank has been explained.
- ☐ Circuit diagram description of current circuit board and voltage sensor board has been discussed.
- ☐ Block diagram of Adaptive Sliding Mode Control (ASMC) based algorithm of VSC-BESS Microgrid system description.
- ☐ Dynamic performance of the SEIG output voltage, output current, PV array output current and battery current.
- ☐ Explained how SEIG based stand alone Microgrid integrates renewable energy sources.

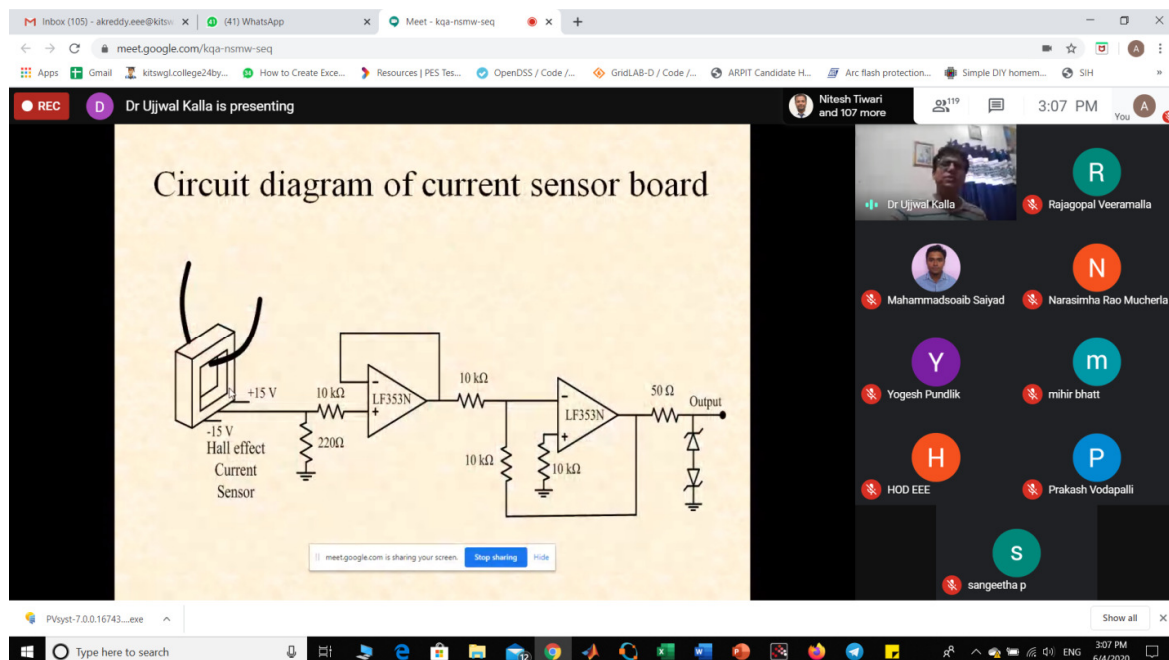
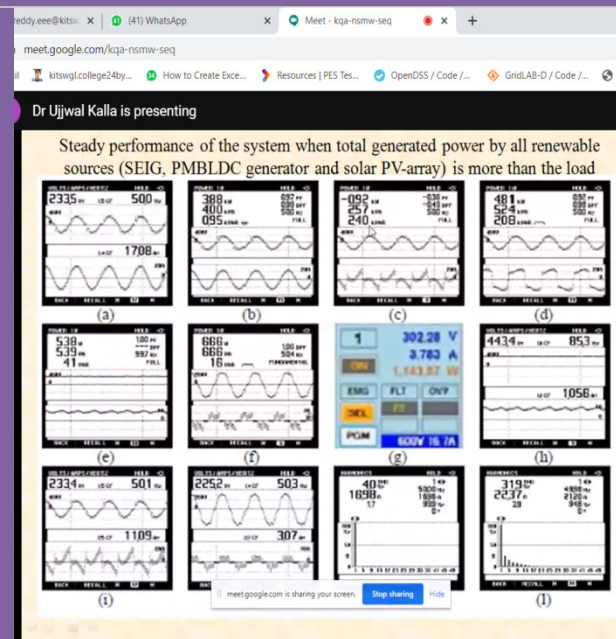
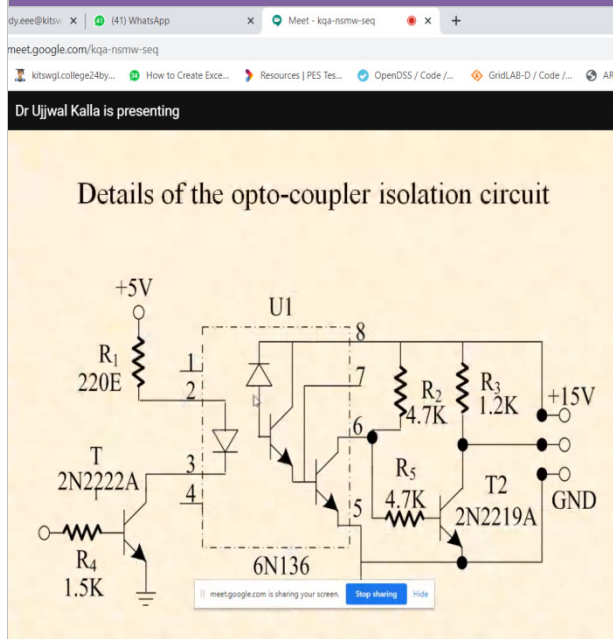
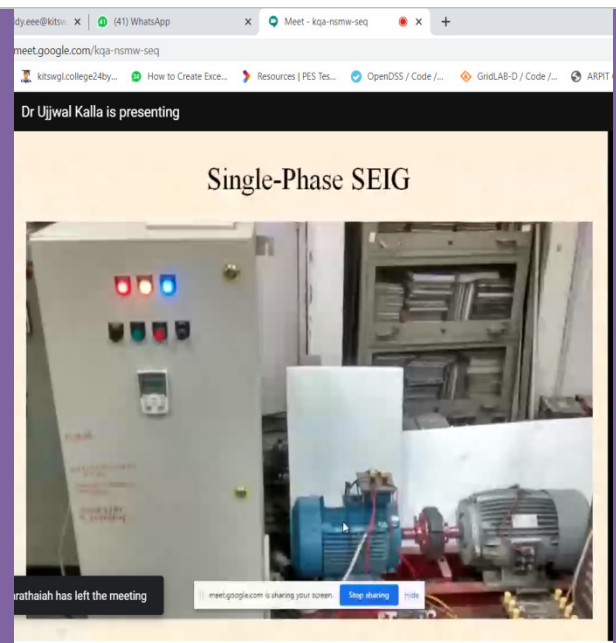
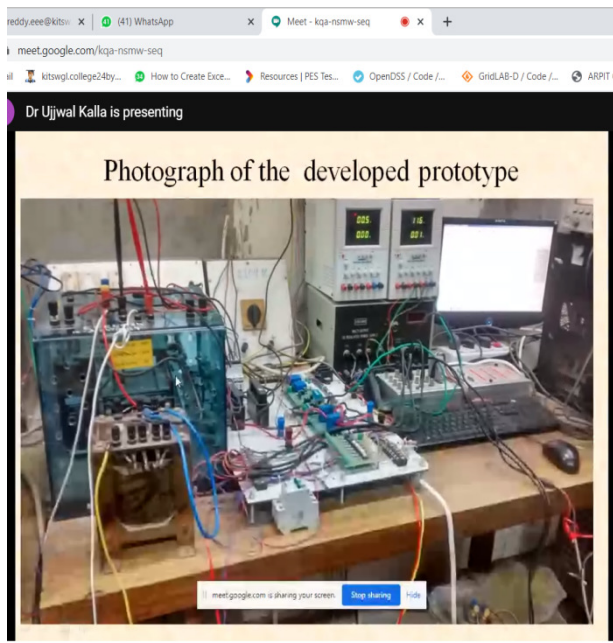


Photo 7: Dr. Ujwal Kalla, while delivering the Lecture



Dr. Ujwal Kalla session screenshots

SESSION: 8

TITLE: Power System Optimization including Renewable Energy Sources

RESOURCE PERSON: Dr. Surender Reddy, Associate Professor, Woosong University, South Korea

REPORT: In this session Dr. Surender Reddy delivered the following points in his lecture.

- ☐ Fundamental concepts of optimal power flow have been discussed.
- ☐ The impact of wind power uncertainty in the OPF problem has been explained.
- ☐ Energy & Spinning reserves cost minimization as one of the objectives.
- ☐ System risk level minimization as another objective.
- ☐ Probability density function and load forecast uncertainty model.
- ☐ Equality and inequality constraints for optimization problems.
- ☐ Concept of total cost minimization with uncertainty in wind generation.
- ☐ Case studies on IEEE 30 bus system for considering models are explained.

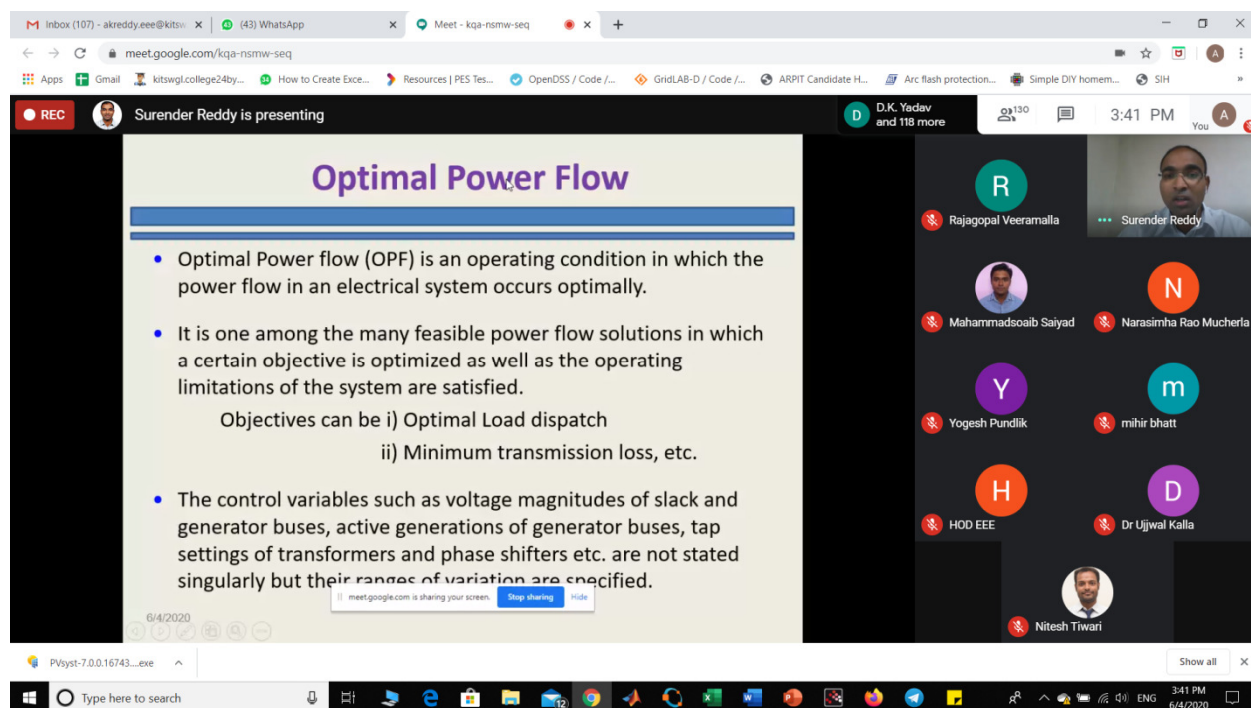
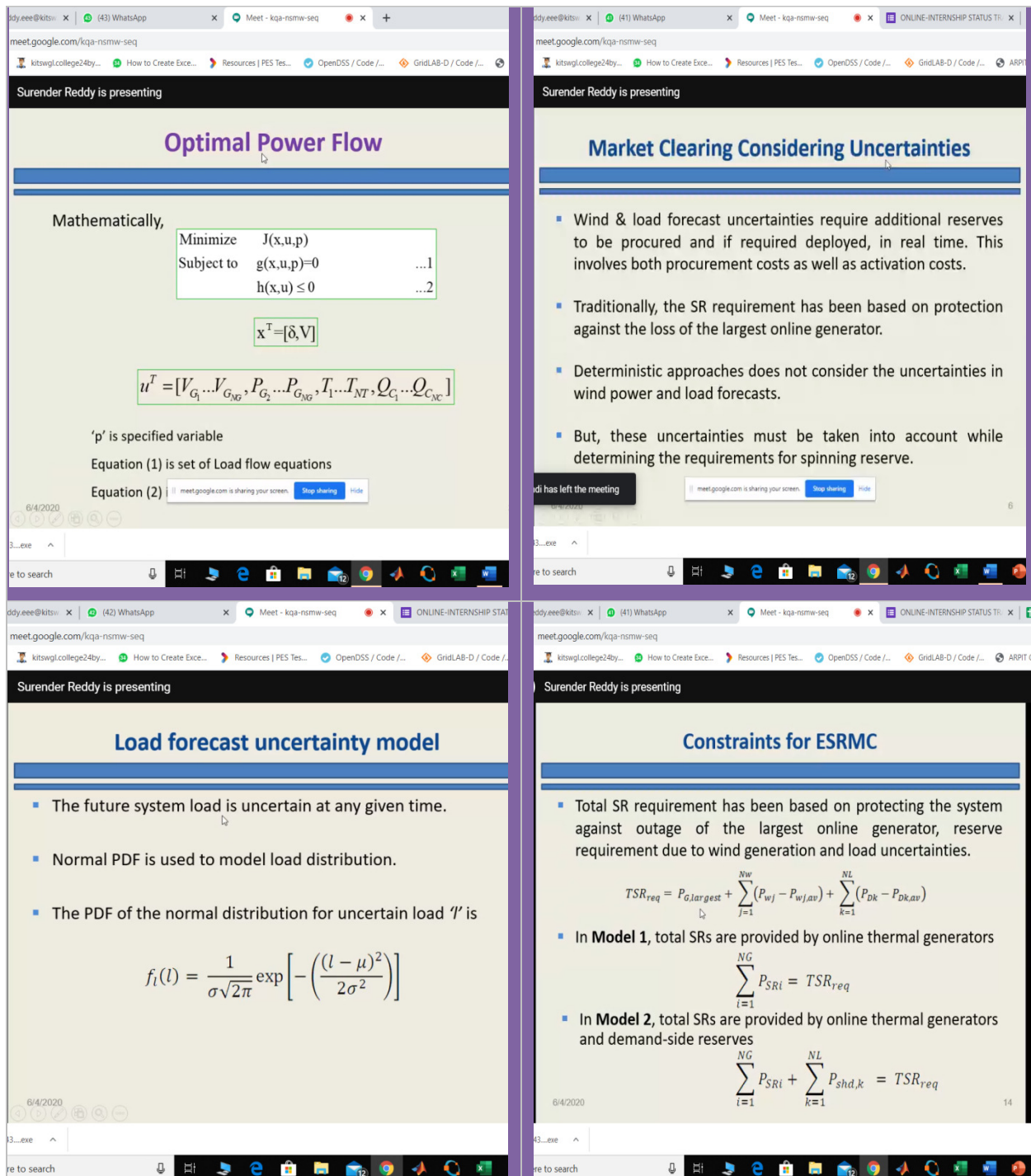


Photo 8: Dr. Surender Reddy, while delivering the Lecture



Dr. Surender Reddy session screen shots

SESSION: 9

TITLE: Wireless Charging of EVs

RESOURCE PERSON: Dr. Phaneendra Babu Bobba, Professor, GRIET Hyderabad

REPORT: In this session Dr. Phaneendra Babu Bobba delivered the following points in his lecture.

- ☐ Main features of WPS system have been explained.
- ☐ Comparison between conductive and inductive charging has shown.
- ☐ Concept of static wireless charging of Electric Vehicles (EVs).
- ☐ Concept of dynamic wireless charging of Electric Vehicles (EVs).
- ☐ Standards and specifications for wired and wireless powered EVs.
- ☐ Basic schematics of different WPT techniques.
- ☐ Need for compensation topologies and different compensation topologies have been discussed.
- ☐ Designing of wireless charging systems has been described.

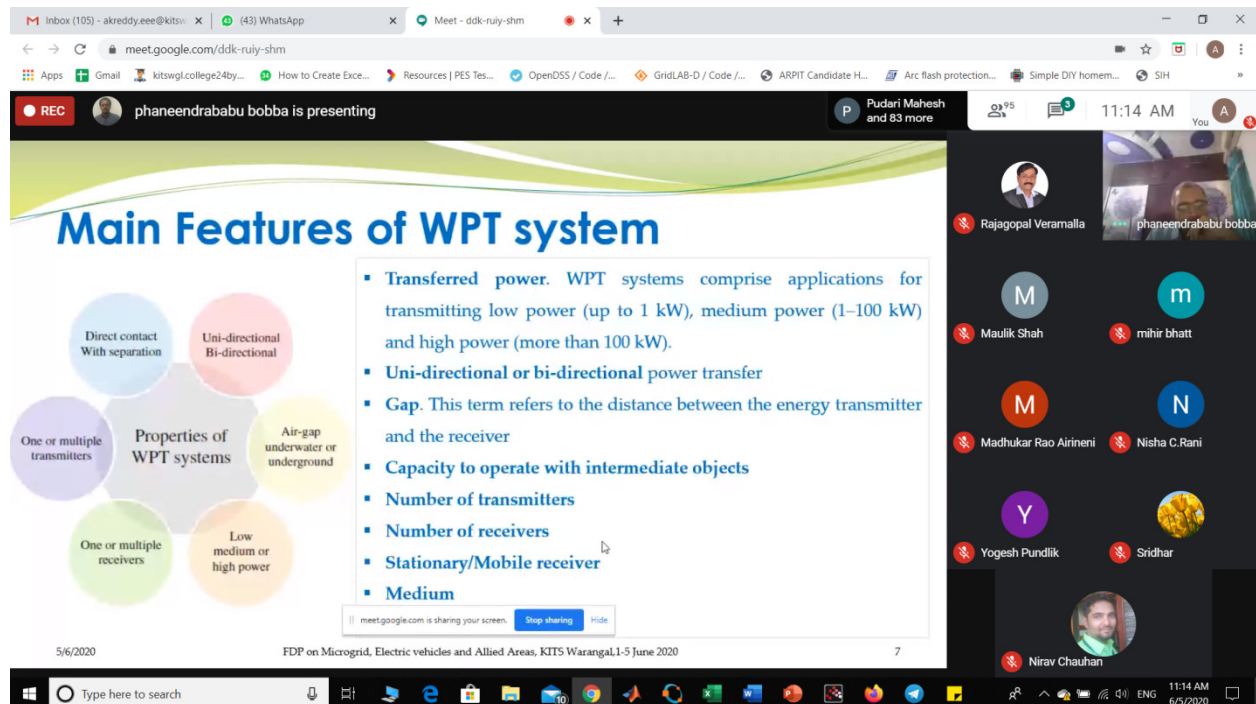


Photo 9: Dr. Phaneendra Babu Bobba, while delivering the Lecture

phaneendrababu bobba is presenting

Introduction

Examples of modern movable things that need seamless electric power

A general classification of power transfer in terms of mobility, distance, and means of powering.

Stationary PT: Fixed PT - HV power line, Street light, etc.; Detachable PT - Cable EV charger/Electric shaver, etc.

Power Transfer: Inductive PT - Wireless EV, Mobile device, etc.; Capacitive PT - Transfer of fluid; Conductive PT - Electric bus/train.

Mobile PT: RF PT - Wi-Fi, cellular, etc.; Optical PT - Li-Fi, Li-Fi, etc.; Tethered PT - UAV, UAV, UAV.

Wireless PT

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phaneendrababu bobba is presenting

Factors effecting WPTs

Factors effecting WPTs

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Static wireless charging of EVs

SWC Representation

SWC schematic along with DC-DC efficiency

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Dynamic wireless charging of EVs

Basic representation of a single loop track

Basic representation of segmented transmitter tracks

5/6/2020 FDP on Microgrid, Electric vehicles and Allied Areas, KITS Warangal, 1-5 June 2020 14

Dr. Phaneendra Babu session screen shots

DATE: 05-06-2020

TIME: 2.30 – 3.30pm

SESSION: 10

TITLE: Battery Management System for EVs

RESOURCE PERSON: Dr. Kalpana Ramesh Babu, Assistant Professor, NIT Surathkal

REPORT: In this session Dr. Kalpana Ramesh Babu delivered the following points in his lecture.

- ☐ Significance and challenges to Lithium batteries have been explained.
- ☐ Lithium ion cells configuration in series and parallel combination.
- ☐ Charging and discharging of Lithium ion batteries.
- ☐ Batteries definition interns of voltage and Capacity.
- ☐ Definition and Essence of Battery Management System.
- ☐ Concept of battery equivalent circuit modeling.
- ☐ Battery State estimation and State Of Charge (SOC)
- ☐ DC resistance based SOH estimation technique explanation.

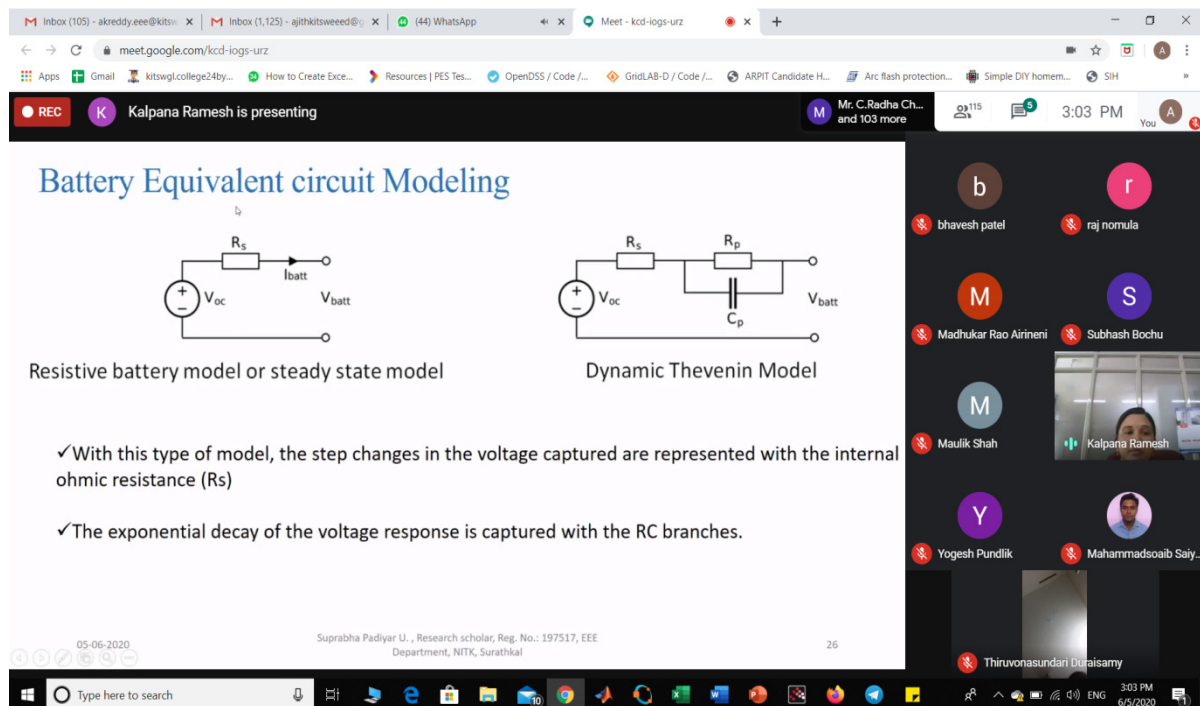


Photo 10: Dr. Kalpana Ramesh Babu, while delivering the Lecture

Battery Storage System

Parameters	Lithium -Ion	Lead Acid
Light on Weight	typically weigh one-third less	Weights more
Heavy on Power	50% more energy	Less energy
Highly Efficient	super-low resistance and 95% efficiency	80-85%
Ultra Long Life	batteries cycle 5,000 times or more	Lead-acid batteries typically deliver only 300-500 cycle
Usable energy	80%	50%
Voltage per cell	3.6V	2V
Cost	More	less
Maintenance Requirements	Basic annual Maintenance	Regular Maintenance every 3 months

LITHIUM Battery Challenges

- ✓ **Expensive to manufacture** - about 50 % higher in cost than lead acid batteries.
- ✓ **Protection required** - Requires protection circuit to maintain voltage and current within safe
- ✓ **Sensitivity to high temperature** – Overheating or overcharging causes the cells or packs of this battery to degrade faster.
- ✓ **Subject to aging effect** - even if not in use - storage in a cool place at 40% charge.
- ✓ **Transportation restrictions** - shipment of larger quantities may be subject to regulatory control. This restriction does not apply to personal carry-on batteries.

Basics of Battery

Li-Ion cells in series connection

- Portable equipment needing higher voltages use battery packs with two or more cells connected in series.
- Each Cell of 3400mAh; 3400mAh=3.4Ah
- A battery pack with four 3.6V Li-ion cells in series, also known as 4S, to produce 14.4V nominal
- In comparison, a six-cell lead acid string with 2V/cell will generate 12V, and four alkaline with 1.5V/cell will give 6V.

How a lithium-ion battery charges and discharges ?

- Lithium-ion uses a cathode (positive electrode), an anode (negative electrode) and electrolyte as conductor.
- The cathode is metal oxide and the anode consists of porous carbon.
- When the cell charges and discharges, ions shuttle between cathode and anode.
- During discharge, the ions flow from the anode to the cathode through the electrolyte and separator; charge reverses the direction and the ions flow from the cathode to the anode.

Dr. Kalpana Ramesh Babu session screen shots

SESSION: 11

TITLE: Advanced Power Electronics Applications in Aerospace, EV and Renewable Energy

RESOURCE PERSON: Dr. Sandeep Madishetti, Research Scientist, EPGC, ERI@NTU, Singapore

REPORT: In this session Dr. Sandeep Madishetti delivered the following points in his lecture.

- ☐ EPGC core capabilities explained with the help of a schematic diagram.
- ☐ Aircraft electrification market and description of various Aircrafts.
- ☐ The concept of electric power in Aircraft and power system architecture in Aircraft.
- ☐ Explained various research areas in Aircraft Power system.
- ☐ Different types of Electric Vehicles.
- ☐ Concept of BEV architecture with schematic diagram.
- ☐ SiC and GaN power components for BEV and PHEV.
- ☐ Explanation on high power density and high efficient converters.

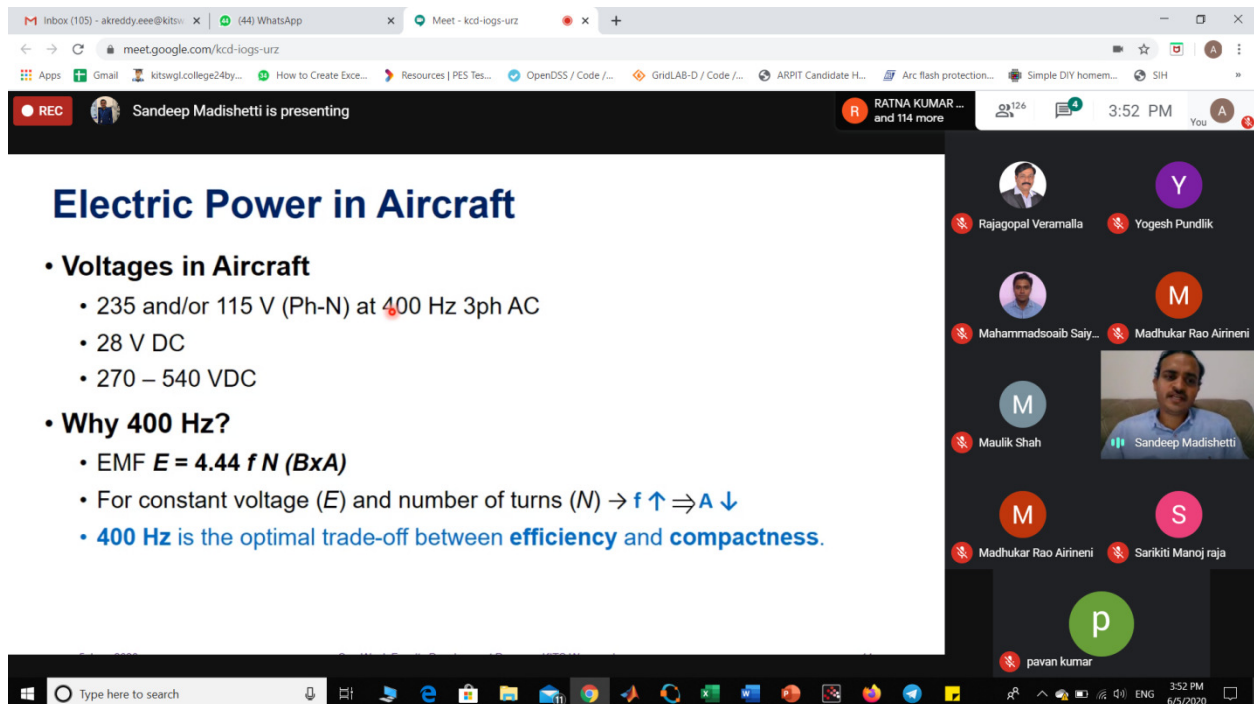


Photo 11: Dr. Sandeep Madishetti, while delivering the Lecture

Aircraft Power System Architecture

The diagram illustrates the power system architecture of an aircraft. It starts with the **Aircraft Engine**, which is connected to a **Syn Generator**. The generator outputs **235 V (Ph-N), 350-800 Hz 3-Ph AC**. This AC power is then converted into three different output types, each represented by a yellow box with a tilde symbol and an equals sign:

- ±270 V DC**: A DC output.
- 28 V DC**: A low-voltage DC output.
- 115 V AC (Ph-N), 400 Hz, 3-Ph AC**: A three-phase AC output.

Below the diagram, a text box states: **Various Power Electronic converters (DC-DC, DC-AC, AC-DC) are used from generation to the in-flight entertainments.**

GaN based AC-DC Converter for Aircraft

- AES PS2500 Switch Mode Power Supply
 - Input: 96-130 VAC/360 Hz – 800 Hz,
 - Output: 28 VDC, 42 A (1200W)
 - PF: 0.98,
 - Efficiency: **91.5%** (**11.5%** more than the **Si** equivalent)
 - Weight: 4kg
 - Used in CS-25 airplane manufacturers (e.g., Airbus A318-A321, A330, A340, **A380** and Boeing B767, **B787** VIP aircraft)

A photograph of the AES PS2500 Switch Mode Power Supply unit. It is a rectangular, silver-colored metal enclosure with a black front panel. The front panel features two large black circular connectors on the right side and a small red LED indicator. The top of the unit is labeled "AES PS2500".

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DATE: 05-06-2020
TIME: 4.30 – 4.45pm

SESSION: II

TITLE: Valedictory

- Principal, Professor K. Ashoka Reddy has congratulated all the participants, Coordinators and convener for successful completion of FDP.



Prof. K. Ashoka Reddy, while speaking in the Valedictory

**ISTE Sponsored One Week Faculty Development Program (Online) on
Micro Grid, Electric Vehicles and Allied Areas**

1st- 5th June 2020

Organized by

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

List of Participants

Email Address	NAME OF THE PARTICIPANT IN BLOCK LETTERS	Designation of the Participant	Name of the Institute / Organization	Whatsapp Mobile number	Certificate Number
202chandar@gmail.com	BHANUCHANDAR	Research Scholar	NIT Warangal	9652447125	KITSW/EEED/FDP /MGEVAA20/P001
abhiantham@gmail.com	ABISHEKERREDDY ANTHAM	Assistant Professor	Kamala institute of technology and science	9951887772	KITSW/EEED/FDP /MGEVAA20/P002
amr.eee@kitsw.ac.in	Dr.A.Madhukar Rao	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE, WARANGAL	9963902827	KITSW/EEED/FDP /MGEVAA20/P003
anshvarshney@gmail.com	ANSHUL VARSHNEY	Research Scholar	IIT Delhi	9711489895	KITSW/EEED/FDP /MGEVAA20/P004
aravinddprec@gmail.com	ARAVINDA SWAMY KONDA	Assistant Professor	MRCET, UGC AUTONOMOUS SECENDRABAD	7382715484	KITSW/EEED/FDP /MGEVAA20/P005
archana.pothuganti@gmail.com	ARCHANA POTHUGANTI	Assistant Professor	Vaagdevi College Of Engineering Bolikunta Warangal	7981664742	KITSW/EEED/FDP /MGEVAA20/P006
arulkumarme@gmail.com	ARULKUMAR.P	Associate Professor	Balaji Institute of Technology and Science, Warangal	9842654673	KITSW/EEED/FDP /MGEVAA20/P007
devivighneshwari@gmail.com	B.DEVI VIGHNESHWARI	Associate Professor	The Oxford College of Engg	9342129484	KITSW/EEED/FDP /MGEVAA20/P008
subashbochu@gmail.com	B.SUBHASH	Associate Professor	CITS (T.S.)	9866576101	KITSW/EEED/FDP /MGEVAA20/P009
goldeneagle.raj87@gmail.com	BURRA BALA RAJU	HEAD OF SECTION / EEE	V M R POLYTECHNIC : RAMPUR :: WARANGAL	9849270631	KITSW/EEED/FDP /MGEVAA20/P010
cv.eee@kitsw.ac.in	CHALLA VENKATESH	Professor	Kakatiya Institute of Technology and Science, Warangal	9440031922	KITSW/EEED/FDP /MGEVAA20/P011
sunilkumar.ch@bvrithyderabad.edu.in	CHAVA SUNIL KUMAR	Professor	BVRIT HYDERABAD College of Engineering for Women	9440545949	KITSW/EEED/FDP /MGEVAA20/P012
pavan24eee@gmail.com	CHAVALI PAVAN KUMAR	Assistant Professor	Sree Vahini Institute of Science & Technology Tiruvuru	9440870802	KITSW/EEED/FDP /MGEVAA20/P013
cheralu.333@gmail.com	CHERALU BAKKA	Assistant Professor	Kamala Institute of Technology&Science, Singapur, Huzurabad.	9849185158	KITSW/EEED/FDP /MGEVAA20/P014
gutha26@gmail.com	Dr G NAVEEN KUMAR	Associate Professor	Andhra Loyola Institute of Engineering and Technology	7981178039	KITSW/EEED/FDP /MGEVAA20/P015
goli.raviprakash@gmail.com	Dr G RAVI KUMAR	Professor	Bapatla Engineering College	9492466110	KITSW/EEED/FDP /MGEVAA20/P016
gsn.anusree@gmail.com	Dr GOTURU SREENIVASAN	Professor	Srinivasa Ramanujan Institute of Technology, Anantapur, AP	9440760073	KITSW/EEED/FDP /MGEVAA20/P017
lsrlingineni@yahoo.co.in	Dr L SHANMUKHA RAO	Professor	KALLAM HARANADHAREDDY INSTITUTE OF TECHNOLOGY, GUNTUR, ANDHRA PRADESH	9440372593	KITSW/EEED/FDP /MGEVAA20/P018

mssivagamasundari@gmail.com	Dr M S SIVAGAMA SUNDARI	Assistant Professor	Amrita College of Engineering and Technology, Nagercoil	6383966254	KITSW/EEED/FDP /MGEVAA20/P019
gr.eee@kitsw.ac.in	Dr RAJENDAR GOGU	Associate Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9849570620	KITSW/EEED/FDP /MGEVAA20/P020
ramdeshmukh@gmail.com	Dr Ram Deshmukh	Professor	SR Engineering College	9704955020	KITSW/EEED/FDP /MGEVAA20/P021
bairuvijaykumar@gmail.com	Dr. B. VIJAY KUMAR	Associate Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9963310059	KITSW/EEED/FDP /MGEVAA20/P022
bhinal.pdpu@gmail.com	Dr. BHINALKUMAR BAKULBHAI MEHTA	Assistant Professor	Pandit Deendayal Petroleum University	9427045058	KITSW/EEED/FDP /MGEVAA20/P023
reddy.lokeshwar@gmail.com	Dr. CH. LOKESHWAR REDDY	Professor	CVR COLLEGE OF ENGINEERING	9701114325	KITSW/EEED/FDP /MGEVAA20/P024
dkyadav@rtu.ac.in	Dr. DINESH KUMAR YADAV	Associate Professor	Rajasthan Technical University Kota	9414453513	KITSW/EEED/FDP /MGEVAA20/P025
sekhararao_eee@mvsrec.edu.in	Dr. E.V.C.SEKHARA RAO	Associate Professor	Maturi Venkata Subba Rao (MVS) Engineering College, Hyderabad	9290448153	KITSW/EEED/FDP /MGEVAA20/P026
venumadhav@cvsr.ac.in	Dr. GOPALA VENU MADHAV	Professor	ANURAG GROUP OF INSTITUTIONS VENKATAPUR GHATKESAR	9848749953	KITSW/EEED/FDP /MGEVAA20/P027
kintalisuneeta@gmail.com	Dr. K SUNEETA	Associate Professor	JB Institute of Engineering and Technology	9398549836	KITSW/EEED/FDP /MGEVAA20/P028
rayudukaturi@gmail.com	Dr. KATURI RAYUDU	Professor	BVRIT -NARSAPUR	9959599839	KITSW/EEED/FDP /MGEVAA20/P029
ajaykumar@aliet.ac.in	Dr. M. AJAY KUMAR	Associate Professor	Andhra Loyola Institute of Engineering and Technology, Vijayawada	9491827298	KITSW/EEED/FDP /MGEVAA20/P030
sridhar@iare.ac.in	Dr. PATTHI SRIDHAR	Professor	Institute of Aeronautical Engineering, Hyderabad	8886023000	KITSW/EEED/FDP /MGEVAA20/P031
ujjaval58@rediffmail.com	Dr. Ujjaval Patel	Assistant Professor	Adani Institute of Infrastructure Engineering	9879879746	KITSW/EEED/FDP /MGEVAA20/P032
vrg.eee@kitsw.ac.in	Dr. V.RAJAGOPAL	Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9392114063	KITSW/EEED/FDP /MGEVAA20/P033
sudheavv@gmail.com	Dr. VEERA VENKATA SUDHAKAR ANGATHA	Associate Professor	S R Engineering College, Warangal	9000708123	KITSW/EEED/FDP /MGEVAA20/P034
rajendrababu77@gmail.com	Dr. Y. RAJENDRA BABU	Professor	PSCMRCET, Vijayawada (A. P)	9290509415	KITSW/EEED/FDP /MGEVAA20/P035
sailajacvss@gmail.com	Dr.Ch.V.S.S.Sailaja	Associate Professor	Vasavi College of Engineering, (T.S.)	8121981253	KITSW/EEED/FDP /MGEVAA20/P036
gnrgudipudi@gmail.com	Dr.G.NAGESWARA RAO	Professor	Lakireddy Bali Reddy College of Engineering	9247876900	KITSW/EEED/FDP /MGEVAA20/P037
ninjarap@gitam.edu	Dr.I E SANYASI NAIDU	Associate Professor	GITAM Institute of Technology, Visakhapatnam	9.1944E+11	KITSW/EEED/FDP /MGEVAA20/P038
kathirvele@srec.ac.in	Dr.Kathirvel Chinnasamy	Associate Professor	Sri Ramakrishna Engineering College	9994903511	KITSW/EEED/FDP /MGEVAA20/P039
kappagantulasubbu@gmail.com	Dr.KBVS SUBRAHMANYAM	Associate Professor	S R ENGINEERING COLLEGE, WARANGAL	9949746279	KITSW/EEED/FDP /MGEVAA20/P040

mjeesan07@gmail.com	Dr.M.JEGADEESAN	Associate Professor	K.L.N.COLLEGE OF ENGINEERING	9003776136	KITSW/EEED/FDP /MGEVAA20/P041
sarulakumar.bvts@bvcgroup.in	Dr.S.ARULKUMAR	Professor	BVCITS AMALAPURAM	9962360136	KITSW/EEED/FDP /MGEVAA20/P042
kumarasanjay@gmail.com	Dr.Sanjay Kumar	Associate Professor	Centurion University of Technology and Management,Odisha,India	9559226232	KITSW/EEED/FDP /MGEVAA20/P043
nireekshana_t@vnrvjiet.in	Dr.Turaka Nireekshana	Associate Professor	VNR Vignana Jyothi Institute of Engineering and Technology	9703013127	KITSW/EEED/FDP /MGEVAA20/P044
sv.padmavathi@gmail.com	Dr.VENKATA PADMAVATHI S	Assistant Professor	GITAM School of Technology, GITAM Deemed to be University,Hyderabad campus	9948016232	KITSW/EEED/FDP /MGEVAA20/P045
venufacts20m@gmail.com	Dr.VENUGOPAL DUGYALA	Associate Professor	KITS, Singapuram,(T.S)	9989418188	KITSW/EEED/FDP /MGEVAA20/P046
yogesh4037@rediffmail.com	Dr.YOGESH YASHWANT PUNDLIK	Professor	KAMALA INSTITUTE OF TECHNOLOGY AND SCIENCE, SINGAPUR-505468	9701167259	KITSW/EEED/FDP /MGEVAA20/P047
durgamrajababu@gmail.com	DURGAM RAJABABU	Associate Professor	S R Engineering College	9949501190	KITSW/EEED/FDP /MGEVAA20/P048
gs.eee@kitsw.ac.in	G SUDHEER KUMAR	Associate Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9923951236	KITSW/EEED/FDP /MGEVAA20/P049
rajalaxmi3@gmail.com	GUJJIGA RAJALAXMI	Assistant Professor	Tirumala Engineering College,Keesara,Bogaram,RR dist	6303314208	KITSW/EEED/FDP /MGEVAA20/P050
muralimallika@gmail.com	J Easwara Muralidhar	Associate Professor	Muffakhamjah College of Engineering and Technology	9866277830	KITSW/EEED/FDP /MGEVAA20/P051
jaijagadeesho@gmail.com	JAI JAGADEESH O	Assistant Professor	Proudhadevaraya Institute of Technology Hosapete	7813808537	KITSW/EEED/FDP /MGEVAA20/P052
njktry78@gmail.com	JAYAKUMAR . N	Associate Professor	The Oxford College of Engineering ,Bangalore	8050837275	KITSW/EEED/FDP /MGEVAA20/P053
jkumbhare04@gmail.com	JYOTI KUMBHARE	Assistant Professor	Yeshwantrao Chavan College of Engineering Nagpur (YCCE NAGPUR)	9850759324	KITSW/EEED/FDP /MGEVAA20/P054
katta040@gmail.com	K SRINIVASA RAO	Assistant Professor	Bharat institute of engineering and technology	9989181305	KITSW/EEED/FDP /MGEVAA20/P055
sadanandam4u@gmail.com	KADASI SADANANDAM	Assistant Professor	JNTUH COLLEGE OF ENGINEERING MANTHANI	8801470305	KITSW/EEED/FDP /MGEVAA20/P056
maheshkanneboina21@gmail.com	KANNEBOINA MAHESH	Assistant Professor	TKR college of engineering and technology	9912765583	KITSW/EEED/FDP /MGEVAA20/P057
srinivaskaratlapelli@gmail.com	KARATLAPELLY SRINIVAS	Assistant Professor	Warangal institute of technology and science	9866636472	KITSW/EEED/FDP /MGEVAA20/P058
karthi2eee@gmail.com	KARTHIKEYAN V	Assistant Professor	ADHIYAMAAN COLLEGE OF ENGINEERING	8300088814	KITSW/EEED/FDP /MGEVAA20/P059
nishanthkatam@gmail.com	Katam Nishanth	Research Scholar	IISc Bangalore	9441843624	KITSW/EEED/FDP /MGEVAA20/P060
akreddy.eee@kitsw.ac.in	KATANGURI AJITH	Assistant Professor	Kakatiya Institute of Technology and Science Warangal	9573327223	KITSW/EEED/FDP /MGEVAA20/P061
gouda402@rymec.in	LINGANAGOUDA R	Assistant Professor	RYM Engineering college Ballari	9844580222	KITSW/EEED/FDP /MGEVAA20/P062
mnalinidevi_eee@mgit.ac.in	M NALINI DEVI	Assistant Professor	Mahatma Gandhi Institute of Technology, Hyderabad, Telangana	9908011263	KITSW/EEED/FDP /MGEVAA20/P063

surendar206@gmail.com	M SURENDAR	Assistant Professor	UCET&W, KAKATIYA UNIVERSITY, WARANGAL	9701574896	KITSW/EEED/FDP /MGEVAA20/P064
san.eee@kitsw.ac.in	M. SANTHOSH	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	8008886804	KITSW/EEED/FDP /MGEVAA20/P065
msp.eee@kitsw.ac.in	M.SPANDANA	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9618081288	KITSW/EEED/FDP /MGEVAA20/P066
snehal.malvi@sal.edu.in	Malvi Snehalkumar Vadilal	Assistant Professor	Sal Institute of Technology and Engineering Research	9722781468	KITSW/EEED/FDP /MGEVAA20/P067
maulikniki@gmail.com	MAULIK J. Shah	Assistant Professor	CSPIT, CHARUSAT	9978441457	KITSW/EEED/FDP /MGEVAA20/P068
imranmohammad5555@gmail.com	MD IMRAN	Assistant Professor	VAAGESWARI COLLEGE OF ENGINEERING	9490581093	KITSW/EEED/FDP /MGEVAA20/P069
jawahar.a@pec.edu	Mr A JAWAHAR	Research Scholar	Pondicherry Engineering College	7702743855	KITSW/EEED/FDP /MGEVAA20/P070
boyinaguruseshu015@gmail.com	Mr BOYINA GURUESHU	Assistant Professor	PRAKASAM ENGINEERING COLLEGE KANDUKUR	9573776499	KITSW/EEED/FDP /MGEVAA20/P071
srisailamgnitc@gmail.com	Mr CH SRISAILAM	Assistant Professor	GURU NANAK INSTITUTION TECHNICAL CAMPUS	9949747806	KITSW/EEED/FDP /MGEVAA20/P072
sravaneestra@gmail.com	Mr PALARAPU SRAVAN KUMAR	Assistant Professor	Bharat Institute Of Engineering And Technology , Ibrahimptnam	9666645035	KITSW/EEED/FDP /MGEVAA20/P073
suresh.alldhandi@gmail.com	Mr. A. SURESH	Assistant Professor	Chaitanya Institute of Technology and Science	9912783105	KITSW/EEED/FDP /MGEVAA20/P074
avrk17482@gmail.com	Mr. ALLU VENKATA RAVI KUMAR	Assistant Professor	DVR & Dr.HS MIC College of Technology	9032988733	KITSW/EEED/FDP /MGEVAA20/P075
parsumetme@gmail.com	Mr. B. PARASURAM	Associate Professor	Bheema Institute of technology & sciences, ADONI	9052119956	KITSW/EEED/FDP /MGEVAA20/P076
bhaveshele99@gmail.com	Mr. BHAVESH SHANKARBHAI PATEL	Assistant Professor	GIDC DEGREE ENGINEERING COLLEGE ABRAMA, NAVSARI	9033331505	KITSW/EEED/FDP /MGEVAA20/P077
dina4karan@gmail.com	Mr. C DINAKARAN	Assistant Professor	Audisankara College of Engineering & Technology, Gudur	9701605825	KITSW/EEED/FDP /MGEVAA20/P078
dinakaransank@gmail.com	Mr. C DINAKARAN	Assistant Professor	Audisankara College of Engineering & Technology, Gudur	9701605825	KITSW/EEED/FDP /MGEVAA20/P079
crsp143@gmail.com	Mr. CHINMAYA RANJAN PRADHAN	Assistant Professor	NM INSTITUTE OF ENGINEERING & TECHNOLOGY, BHUBANESWAR	9439365218	KITSW/EEED/FDP /MGEVAA20/P080
ashok7200@gmail.com	Mr. G. ASHOK KUMAR	Assistant Professor	Trinity College of Engineering and Technology, Peddapalli	9553143133	KITSW/EEED/FDP /MGEVAA20/P081
rajashekar.iruvanti@gmail.com	Mr. IRUVANTI RAJASHEKAR	Assistant Professor	MALLA REDDY COLLEGE OF ENGINEERING AND TECHNOLOGY	8125291903	KITSW/EEED/FDP /MGEVAA20/P082
jagdishdholwani@gmail.com	MR. JAGDISH PITAMBAR DHOLWANI	Research Scholar	NIT Silchar Assam	8856866809	KITSW/EEED/FDP /MGEVAA20/P083
dhanraj12k@gmail.com	Mr. K. DHANRAJ	Assistant Professor	Talla Padmavathi College of Engineering	9866155661	KITSW/EEED/FDP /MGEVAA20/P084
skjntum@gmail.com	Mr. KUMAR SALIGANTI	Assistant Professor	JNTUH COLLEGE OF ENGINEERING MANTHANI	9441110118	KITSW/EEED/FDP /MGEVAA20/P085
mihirbhatt.ee@charusat.ac.in	Mr. M. BHATT	Assistant Professor	CSPIT, Changa	9913277673	KITSW/EEED/FDP /MGEVAA20/P086

mnr.eee@kitsw.ac.in	Mr. M.NARASIMHA RAO	Associate Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9440788094	KITSW/EEED/FDP /MGEVAA20/P087
mahammadsoaibaiyad. ee@charusat.ac.in	Mr. MAHAMMADSOAI B M. SAIYAD	Assistant Professor	CSPIT, CHARUSAT, CHANGA	8485916188	KITSW/EEED/FDP /MGEVAA20/P088
mahi.kodela@gmail.com	Mr. MAHENDER KODELA	Assistant Professor	Vaagdevi college of engineering, warangal	9963198685	KITSW/EEED/FDP /MGEVAA20/P089
mahesh.obannavar@pdi t.ac.in	Mr. MAHESH OBANNAVAR	Engineer	Proudhadevaraya Institute of Technology Hospet	8904243365	KITSW/EEED/FDP /MGEVAA20/P090
msk.eee@kitsw.ac.in	Mr. MAVURAPU SRINIVAS	Assistant Professor	Kakatiya Institute of Technology & Science, warangal, (Telangana)	9640133930	KITSW/EEED/FDP /MGEVAA20/P091
ngangadhar19@gmail.co m	Mr. N.GANGADHAR	Associate Professor	BIT INSTITUTE OF TECHNOLOGY - HINDUPUR	9866265416	KITSW/EEED/FDP /MGEVAA20/P092
kiran.eee28@gmail.com	Mr. NARSINGOJU KIRANKUMAR	Assistant Professor	VAAGESWARI COLLEGE OF ENGINEERING, KARIMNAGAR	9959793748	KITSW/EEED/FDP /MGEVAA20/P093
patnanasai@gmail.com	Mr. P. SAI SRINIVAS	Assistant Professor	MVGR College of Engineering	9440274834	KITSW/EEED/FDP /MGEVAA20/P094
eca.pradeep@gmail.com	Mr. PRADEEP RAMAGIRI	Assistant Professor	Malla Reddy College of Engineering & Technology (A), Maisammaguda	9676623494	KITSW/EEED/FDP /MGEVAA20/P095
pratikmochi.ee@charusa t.ac.in	Mr. PRATIK MOCHI	Assistant Professor	C.S. Patel Institute of Technology, CHARUSAT	9974916313	KITSW/EEED/FDP /MGEVAA20/P096
prem.n.verma@goel.edu .in	Mr. PREM NARAYAN VERMA	Assistant Professor	Goel Institute of Technology & Management, Lucknow (Uttar Pradesh)	9454320143	KITSW/EEED/FDP /MGEVAA20/P097
rsksjcsathish@gmail.co m	Mr. RAJA SATHISH KUMAR	Assistant Professor	Keshav Memorial Institute Of Technology Hyderabad	9553271895	KITSW/EEED/FDP /MGEVAA20/P098
satish.ramoji@gmail.co m	Mr. SATISH KUMAR RAMOJI	Research Scholar	National Institute of Technology, Silchar, Assam	9959352279	KITSW/EEED/FDP /MGEVAA20/P099
ksv.eee@kitsw.ac.in	Mr. SRINIVAS KOTTAKONDA	Assistant Professor	Kakatiya Institute of Technology & Science Warangal (KITSW)	9398099230	KITSW/EEED/FDP /MGEVAA20/P100
iamshahbazuddin@gma il.com	Mr. Syed Shahbazuddin	Assistant Professor	Vaagdevi Engineering College, Warangal	8886663203	KITSW/EEED/FDP /MGEVAA20/P101
shajihroyal@gmail.com	Mr. SYED SHAJIH UDDIN AHMED	Assistant Professor	ISL Engineering College	9849427663	KITSW/EEED/FDP /MGEVAA20/P102
tiru.allam225@gmail.co m	Mr. THIRUPATHI ALLAM	Assistant Professor	Kamala Institute of Technology and Science, Huzurabad	9032763884	KITSW/EEED/FDP /MGEVAA20/P103
santha243@gmail.com	Mr. U SHANTHA KUMAR	Assistant Professor	RYMEC, Bellary	9986224671	KITSW/EEED/FDP /MGEVAA20/P104
ylucky2008@gmail.com	Mr. Y. HAZARATHAIAH	Assistant Professor	G PULLAIAH COLLEGE OF ENGINEERING AND TECHNOLOGY	8331903057	KITSW/EEED/FDP /MGEVAA20/P105
santhosh.yeruvaka@gm ail.com	Mr. YERUVAKA SANTHOSH	Assistant Professor	JNTUH COLLEGE OF ENGINEERING MANTHANI	9573848820	KITSW/EEED/FDP /MGEVAA20/P106
shivakumar.avula@gma il.com	Mr.A.SHIVA KUMAR	Assistant Professor	Kommuri Pratap Reddy Institute of Technology, Hyderabad,(TS)	9542090009	KITSW/EEED/FDP /MGEVAA20/P107
jagadeesh908@gmail.co m	Mr.B.JAGADISH KUMAR	Associate Professor	Kakatiya Institute of Technology and Science, Warangal	9440514324	KITSW/EEED/FDP /MGEVAA20/P108
subrahman9am@gmail.c om	Mr.B.SUBRAHMAN YAM	Assistant Professor	Swarandhra College of Engineering and Technology	9000681169	KITSW/EEED/FDP /MGEVAA20/P109
kalyanchallapalli@gmail .com	Mr.Ch.NAGA SAI KALYAN	Assistant Professor	VVIT Guntur	8885811784	KITSW/EEED/FDP /MGEVAA20/P110



chvinaykumar_eee@mgit.ac.in	Mr.CH.VINAY KUMAR	Assistant Professor	MAHATMA GANDHI INSTITUTE OF TECHNOLOGY	9492755749	KITSW/EEED/FDP /MGEVAA20/P111
crcharan@jntuh.ac.in	Mr.CHANDRAGIRI RADHACHARAN	Assistant Professor	JNTUH College of Engineering Jagtial,Nachupally,Jagtial,T.S.	9701352066	KITSW/EEED/FDP /MGEVAA20/P112
dharmeshdabhi.ee@charusat.ac.in	Mr.DHARMESH DABHI	Assistant Professor	Chandubhai S Patel Institute of Technology ,Changa	9574018801	KITSW/EEED/FDP /MGEVAA20/P113
amar1434@gmail.com	Mr.G.AMARENDAR	Assistant Professor	Vaagdevi College of engineering	8121981214	KITSW/EEED/FDP /MGEVAA20/P114
khadarbasha233@gmail.com	Mr.KHADARBASHA SHAIK	Assistant Professor	AM Reddy memorial college of engineering and technology	6302463127	KITSW/EEED/FDP /MGEVAA20/P115
matla.raju33@gmail.com	Mr.M.Raju	Assistant Professor	Kamala Institute of Technology and Science,Singapur,Huzurabad	9701652518	KITSW/EEED/FDP /MGEVAA20/P116
praveen.prajwal@gmail.com	Mr.MATTEDA PRAVEEN KUMAR	Associate Professor	SCCE. KARIMNAGAR.T.S	9849214268	KITSW/EEED/FDP /MGEVAA20/P117
saleem238@gmail.com	Mr.MOHAMMAD SALEEM	Associate Professor	AURORA'S ENGINEERING COLLEGE	9966298527	KITSW/EEED/FDP /MGEVAA20/P118
niravachauhan@gmail.com	Mr.NIRAVKUMAR A CHAUHAN	Assistant Professor	Sardar Vallabhbhai Patel Institute of Technology,SVIT-Vasad	9428658638	KITSW/EEED/FDP /MGEVAA20/P119
prakashshanbog@gmail.com	Mr.Prakasha	Assistant Professor	Proudhadevara Institute of Technology	9880443121	KITSW/EEED/FDP /MGEVAA20/P120
gopikrishna82@gmail.com	Mr.R.GOPI KRISHNA	Assistant Professor	Rajiv Gandhi College of Engineering and Technology, Puducherry	9786082552	KITSW/EEED/FDP /MGEVAA20/P121
srinivasmandela2112@gmail.com	Mr.SRINIVAS MANDELA	Assistant Professor	Dr.Lankapalli Bullayya College of Engineering for Women, Visakhapatnam, (A.P)	7416524429	KITSW/EEED/FDP /MGEVAA20/P122
smmmudassir@yahoo.co.in	Mr.Syed Mujtaba Mahdi Mudassir	Assistant Professor	Deccan College of Engineering and Technology	9949222067	KITSW/EEED/FDP /MGEVAA20/P123
ramakrishna.thatipamula@gmail.com	Mr.THATIPAMULA RAMAKRISHNA	Assistant Professor	Kamala Institute of Technology & science	9963866896	KITSW/EEED/FDP /MGEVAA20/P124
sreenivast.eee@hitam.org	Mr.TUMMAPUDI SRINIVASULU	Associate Professor	HITAM	9848574409	KITSW/EEED/FDP /MGEVAA20/P125
uday4318@gmail.com	Mr.UDAY KUMAR NEERATI	Assistant Professor	VASAVI COLLEGE OF ENGINEERING (A), HYDERABAD	9701172500	KITSW/EEED/FDP /MGEVAA20/P126
nishashamin@gmail.com	Mrs NISHA C RANI	Associate Professor	TOCE, Bangalore	9449635342	KITSW/EEED/FDP /MGEVAA20/P127
shalini815@yahoo.com	Mrs V. BABYSHALINI	Assistant Professor	JNTUH college of engineering, jagtial	9949000488	KITSW/EEED/FDP /MGEVAA20/P128
amritha.k@bvrithyderabad.edu.in	Mrs. AMRITHA. K	Associate Professor	BVRIT Hyderabad College of Engineering for Women	9885511580	KITSW/EEED/FDP /MGEVAA20/P129
shreedeeppa227@gmail.com	Mrs. M.R.SREELAKSHMI	Assistant Professor	CMR ENGINEERING COLLEGE	9014418072	KITSW/EEED/FDP /MGEVAA20/P130
rajeshwari.nomula22@gmail.com	Mrs. NOMULA RAJESHWARI	Contract Lecturer	GPT CHERIAL	9866179998	KITSW/EEED/FDP /MGEVAA20/P131
sucharithathakkalapally@gmail.com	Mrs. P. SUCHARITHA	Assistant Professor	Sumathi Reddy Institute Of Technology For Women, warangal, (T.S.)	96262953	KITSW/EEED/FDP /MGEVAA20/P132
chada.prathyusha@gmail.com	Mrs.CHADA PRATHYUSHA	Assistant Professor	KAMALA INSTITUTE OF TECHNOLOGY AND SCIENCE	9949194629	KITSW/EEED/FDP /MGEVAA20/P133






devikamothukuri@gmail.com	Mrs.M.DEVIKA RANI	Assistant Professor	PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY,VIJAYAWADA	9000218909	KITSW/EEED/FDP /MGEVAA20/P134
rudrojumounika@gmail.com	Mrs.R.MOUNIKA	Assistant Professor	Vaagdevi College of engineering	8801959078	KITSW/EEED/FDP /MGEVAA20/P135
vidhyab.rymec@gmail.com	Mrs.Vidhya B	Assistant Professor	Proudhadevaraya Institute of Technology	8050722762	KITSW/EEED/FDP /MGEVAA20/P136
gayathria2420@gmail.com	Ms D.GAYATHRI	Assistant Professor	IFET COLLEGE OF ENGINEERING	9003756482	KITSW/EEED/FDP /MGEVAA20/P137
damu.vasavi@gmail.com	Ms. DAMU VASAVI	Assistant Professor	Raghu Engineering College, Visakhapatnam	7702544506	KITSW/EEED/FDP /MGEVAA20/P138
poojithak.eee@hitam.org	Ms. KANAKAMEDALA POOJITHA	Assistant Professor	HYDERABAD INSTITUTE OF TECHNOLOGY AND MANAGEMENT	7731064169	KITSW/EEED/FDP /MGEVAA20/P139
soumya.p2019@vitstudent.ac.in	Ms. P. SOUMYA	Research Scholar	Vellore Institute of Technology, Vellore	9949435065	KITSW/EEED/FDP /MGEVAA20/P140
bpallavi@stanley.edu.in	Ms.B .Pallavi	Assistant Professor	Stanley College of Engineering and Technology for Women	8985822317	KITSW/EEED/FDP /MGEVAA20/P141
bathulareshma@gmail.com	Ms.B.Reshma	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE - WARANGAL	7673955016	KITSW/EEED/FDP /MGEVAA20/P142
poojanandhikonda@gmail.com	Ms.N.POOJA	Assistant Professor	CJITS	8522835711	KITSW/EEED/FDP /MGEVAA20/P143
nvumaeer@gmail.com	Ms.N.V.UMA MAHESWARI	Assistant Professor	GCE,Bodinayakanur	9786002634	KITSW/EEED/FDP /MGEVAA20/P144
suprithavemula.263@gmail.com	MS.SUPRITHA VEMULA	Assistant Professor	Kamala institute of technology and science	9492839345	KITSW/EEED/FDP /MGEVAA20/P145
pnreddy.eee@kitsw.ac.in	NAGARJUNA REDDY POREDDY	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE - WARANGAL	9908926407	KITSW/EEED/FDP /MGEVAA20/P146
nainijansi217@gmail.com	NAINI JHANSI	Assistant Professor	Kamala institute of technology and science	9704921339	KITSW/EEED/FDP /MGEVAA20/P147
learn.nish@gmail.com	NISHITH SHAHU	Assistant Professor	Neotech Faculty of Diploma Engineering, Vadodara	9.19428E+11	KITSW/EEED/FDP /MGEVAA20/P148
niteshr1990@gmail.com	NITESH TIWARI	Research Scholar	Madan Mohan Malaviya University of Technology Gorakhpur	9458704552	KITSW/EEED/FDP /MGEVAA20/P149
hemakesavulu7@gmail.com	O.HEMAKESAVULU	Associate Professor	Annamacharya Institute of Technology & Sciences,Rajampet	6304547966	KITSW/EEED/FDP /MGEVAA20/P150
mohanojje237@gmail.com	OJJE MOHAN	Assistant Professor	CJITS, JANGAON	9849358305	KITSW/EEED/FDP /MGEVAA20/P151
niranjan.merits@gmail.com	P sai niranjan Kumar	Assistant Professor	G Narayanamma Inst of tech and science	8074560979	KITSW/EEED/FDP /MGEVAA20/P152
psp4india16@gmail.com	P. S. PATIL	Assistant Professor	Yeshwantrao Chavan College of Engineering	8788514759	KITSW/EEED/FDP /MGEVAA20/P153
sangeetha813@gmail.com	P. SANGEETHA	Assistant Professor	JNTUH College of Engineering Jagtial	9704538004	KITSW/EEED/FDP /MGEVAA20/P154
ppurushotham1098@gmail.com	P.PURUSHOTHAM	Student	Mother Theresa institute of engeneering and technology	9347021566	KITSW/EEED/FDP /MGEVAA20/P155
pavanckumar0081947@gmail.com	PAVAN KUMAR C	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9652801085	KITSW/EEED/FDP /MGEVAA20/P156

Poonam.yadav@goel.ed u.in	POONAM YADAV	Assistant Professor	Goel Institute Of Technology and Management Lucknow	8218618701	KITSW/EEED/FDP /MGEVAA20/P157
prakash289@gmail.com	PRAKASH VODAPALLI	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE - WARANGAL	9618781479	KITSW/EEED/FDP /MGEVAA20/P158
pm.eee@kitsw.ac.in	PUDARI MAHESH	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE - WARANGAL	8328281025	KITSW/EEED/FDP /MGEVAA20/P159
Yannareddy.p@gmail.co m	PUTCHAKAYALA YANNA REDDY	Assistant Professor	Swarnandra College of Engineering and technology, Narsapur, AP	9866285492	KITSW/EEED/FDP /MGEVAA20/P160
gry.eee@kitsw.ac.in	RAKESH YADAV GORRE	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE - WARANGAL	9052061103	KITSW/EEED/FDP /MGEVAA20/P161
ramulu.eee@nmrec.edu. in	RAMULU KUMMARIPALLY	Assistant Professor	Nalla Malla Reddy Engineering College, Divyanagar	9908215236	KITSW/EEED/FDP /MGEVAA20/P162
rashmirekha828@gmail. com	RASHMI REKHA BEHERA	Research Scholar	Centurion University of technology and management, Odisha	9997502999	KITSW/EEED/FDP /MGEVAA20/P163
srk232@gmail.com	RATNA KUMAR SINGAVARAPU	Assistant Professor	KMIT Hyderabad, (T.S.)	8142208261	KITSW/EEED/FDP /MGEVAA20/P164
ravi.majji072@gmail.co m	RAVI KUMAR MAJJI	Research Scholar	National Institute of Technology Silchar	9494946075	KITSW/EEED/FDP /MGEVAA20/P165
anjiraj214@gmail.com	S RAMANJANEYULU KORADA	Associate Professor	NADIMPALLI SATYANARAYANA RAJU INSTITUTE OF TECHNOLOGY (NSRIT)	9494575199	KITSW/EEED/FDP /MGEVAA20/P166
ssudharani_eee@mgit.ac .in	S.SUDHARANI	Assistant Professor	Mahatma Gandhi Institute Of Technology	9490520889	KITSW/EEED/FDP /MGEVAA20/P167
sadi_p901@yahoo.co.in	SADANANDAM PERUMANDLA	Associate Professor	Vaagdevi College of Engineering	9948283769	KITSW/EEED/FDP /MGEVAA20/P168
mail2sadasiv@gmail.co m	SADASIVA BEHERA	Research Scholar	NIT Silchar	9853673629	KITSW/EEED/FDP /MGEVAA20/P169
santhosh.santu201@gma il.com	SANTHOSH KUMAR KATKURI	Assistant Professor	Gudlavalleru Engineering College	9490238940	KITSW/EEED/FDP /MGEVAA20/P170
eeehod.sritw@gmail.co m	SHASHI KUMAR REDDY RAVULA	Assistant Professor	SRITW, WARANGAL	9030080201	KITSW/EEED/FDP /MGEVAA20/P171
mshrikriti@gmail.com	SHRIKRUTI S MANAGOLI	Assistant Professor	Basav Engineering School of Technology, Zalki	6360881144	KITSW/EEED/FDP /MGEVAA20/P172
sravanthi314@gmail.co m	SRAVANTHY GADDAMEEDHI	Assistant Professor	SNIST	9100225542	KITSW/EEED/FDP /MGEVAA20/P173
vs.eee@kitsw.ac.in	SRINIVAS VEMULA	Assistant Professor	KITS Warangal, (T.S.)	8008265026	KITSW/EEED/FDP /MGEVAA20/P174
srinivasarao_j@vnrvjiet.i n	SRINIVASA RAO JALLURI	Associate Professor	VNR VJiet	9866849787	KITSW/EEED/FDP /MGEVAA20/P175
suma.kucet@gmail.com	SUMALATHA KALAKOTLA	Assistant Professor	UNIVERSITY COLLEGE OF ENGINEERING, KAKATIYA UNIVERSITY	9966521708	KITSW/EEED/FDP /MGEVAA20/P176
gsk.eee@kitsw.ac.in	SUNIL KUMAR GUNDA	Assistant Professor	Kakatiya Institute of Technology & Science, Warangal	9618931612	KITSW/EEED/FDP /MGEVAA20/P177
praveen3564@gmail.co m	T PRAVEEN KUMAR	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL	9.1999E+11	KITSW/EEED/FDP /MGEVAA20/P178

chaturvedula_eee@acoe.edu.in	U P KUMAR CHATURVEDULA	Associate Professor	ADITYA COLLEGE OF ENGINEERING	7702119117	KITSW/EEED/FDP /MGEVAA20/P179
phanendrababu_eee@cb it.ac.in	VENKATAPHANEN DRABABU N	Assistant Professor	Chaitanya Bharathi Institute of Technology, Hyderabad, Telangana	8096909995	KITSW/EEED/FDP /MGEVAA20/P180
grn.eee@kitsw.ac.in	Dr G. Rajender Naik	Associate Professor	Kakatiya Institute of Technology & Science Warangal	9866351508	KITSW/EEED/FDP /MGEVAA20/P182
vr.eee@kitsw.ac.in	VEERLAPATI RAMAIAH	Professor	Kakatiya Institute of Technology & Science Warangal	9700585856	KITSW/EEED/FDP /MGEVAA20/P183
arsbarc@gmail.com	Dr Rajasekhar Ananthoju	Assistant Professor	Kakatiya Institute of Technology & Science Warangal	8374932678	KITSW/EEED/FDP /MGEVAA20/P184
drc.eee@kitsw.ac.in	Dr.D.Rakesh Chandra	Assistant Professor	Kakatiya Institute of Technology & Science Warangal	9492442236	KITSW/EEED/FDP /MGEVAA20/P185
chinmaydeshpande123 @gmail.com	CHINMAY VIVEK DESHPANDE	Assistant Professor	Zeal Education Society's, Zeal College of Engineering and Research, Narhe, Pune	8956687627	KITSW/EEED/FDP /MGEVAA20/P186
venkat7785@gmail.com	RAJANI VENKATA KRISHNA	Associate Professor	LORDS INSTITUTE OF ENGINEERING AND TECHNOLOGY	9052058520	KITSW/EEED/FDP /MGEVAA20/P187
venumadhavjee@cvsr.a c.in	Dr. GOPALA VENU MADHAV	Professor	ANURAG GROUP OF INSTITUTIONS VENKATAPUR GHATKESAR	9848749953	KITSW/EEED/FDP /MGEVAA20/P188
vieweeeddept@gmail.co m	Mrs.SEELAM.SUJAT HA DEVI	Assistant Professor	Avanthi Institute of Engg and Technology	9848822862	KITSW/EEED/FDP /MGEVAA20/P189
vishwaprakash0078@g mail.com	Mr. VISWAPRAKASH BABU	Associate Professor	JYOTHISHMATHI INSTITUTE OF TECHNOLOGY AND SCIENCE	9866125320	KITSW/EEED/FDP /MGEVAA20/P190
skdwivedi@eed.svnit.ac. in	Shailendra Kumar	Assistant Professor	SVNIT Surat	9716379527	KITSW/EEED/FDP /MGEVAA20/P191
shruthimaku@gmail.co m	M.V.Shruthi	Assistant Professor	Stanley College of Engineering and Technology for Women	9951018521	KITSW/EEED/FDP /MGEVAA20/P192
drkjits@gmail.com	D Rajani Kumar	Associate Professor	Jayamukhi Institute of Technological Sciences	9949781784	KITSW/EEED/FDP /MGEVAA20/P193
ylnrao_eee@mvsrec.edu. in	Y.LAXMI NARASIMHA RAO	Assistant Professor	M.V.S.R Engineering College	9666232440	KITSW/EEED/FDP /MGEVAA20/P194
yms.eee@kitsw.ac.in	Dr.Y.MANJUSREE	Assistant Professor	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE - WARANGAL	9866218295	KITSW/EEED/FDP /MGEVAA20/P195

SAMPLE CERTIFICATE OF PARTICIPANT AND RESOURCE PERSON

	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE WARANGAL- 506 015 (An AUTONOMOUS Institute Under Kakatiya University, Warangal) DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING	
<i>PARTICIPATION CERTIFICATE</i>		
No: KITSW/EEED/FDP/MGEVAA20/P001		
This is to certify that <u>A.BHANUCHANDAR</u> working as <u>Research Scholar</u> in <u>NIT Warangal</u> has actively participated in ISTE sponsored one week online faculty development program on “MICRO GRID, ELECTRIC VEHICLES AND ALLIED AREAS (MGEVAA-20)” organized by Department of Electrical & Electronics Engineering held during 1st to 5th June, 2020.		
 Prof. V. Rajagopal Coordinator, MGEVAA-20 Professor, EEED, KITSW.	 Prof. C. Venkatesh Convenor, MGEVAA-20 Professor & HoD, EEED, KITSW.	 Prof. K. Ashoka Reddy Principal, KITSW.

	KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE WARANGAL- 506 015 (An AUTONOMOUS Institute Under Kakatiya University, Warangal) DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING	
<i>CERTIFICATE OF APPRECIATION</i>		
No: KITSW/EEED/FDP/MGEVAA20/RP01		
This is to certify that <u>Dr. M. MITHUN BHASKAR</u> working as <u>Head, Model Based Design</u> in <u>TATA ELXSI</u> has delivered a lecture on <u>xEV Autonomy, Retrofitting and MBD</u> in ISTE sponsored one week online faculty development program on “MICRO GRID, ELECTRIC VEHICLES AND ALLIED AREAS (MGEVAA-20)” organized by Department of Electrical & Electronics Engineering held during 1st to 5th June, 2020.		
 Prof. V. Rajagopal Coordinator, MGEVAA-20 Professor, EEED, KITSW.	 Prof. C. Venkatesh Convenor, MGEVAA-20 Professor & HoD, EEED, KITSW.	 Prof. K. Ashoka Reddy Principal, KITSW.

Sample Feedback Form

KITSW_EEED_MGEVAA_FDP_Day-5(Afternoon Session)_Feedback form

Email ID: *

nishanthkatam@gmail.com

Full Name of the Participant (Required as per certificate) *

Katam Nishanth

Designation *

- ☐ Professor
- ☐ Associate Professor
- ☐ Assistant Professor
- ☒ Research Scholar
- ☐ Others

Name of the Department *

Electrical Engineering

Name of Organisation / Institute: *

IISc Bangalore

Place of work *

Bangalore

State *

Karnataka

Mobile Number *

9441843624

How would you rate the Session-1? (Dr. Kalpana Ramesh Babu -Battery management System for EVs) *

Poor 1 2 3 4 5 Excellent

☐ ☐ ☐ ☐ ☒

How would you rate the Session-2? (Dr. Sandeep Madishetti -Advanced Power Electronics Applications in Aerospace, EV and Renewable Energy) *

Poor 1 2 3 4 5 Excellent

☐ ☐ ☐ ☐ ☒

Any suggestions you would like to give:

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